

Compal Confidential

PEW71_81_91 UMA <LA-6582P> M/B Schematics Document

Intel Arrandale Processor with DDRIII + Ibex Peak-M

2010-06-18

REV:0.2

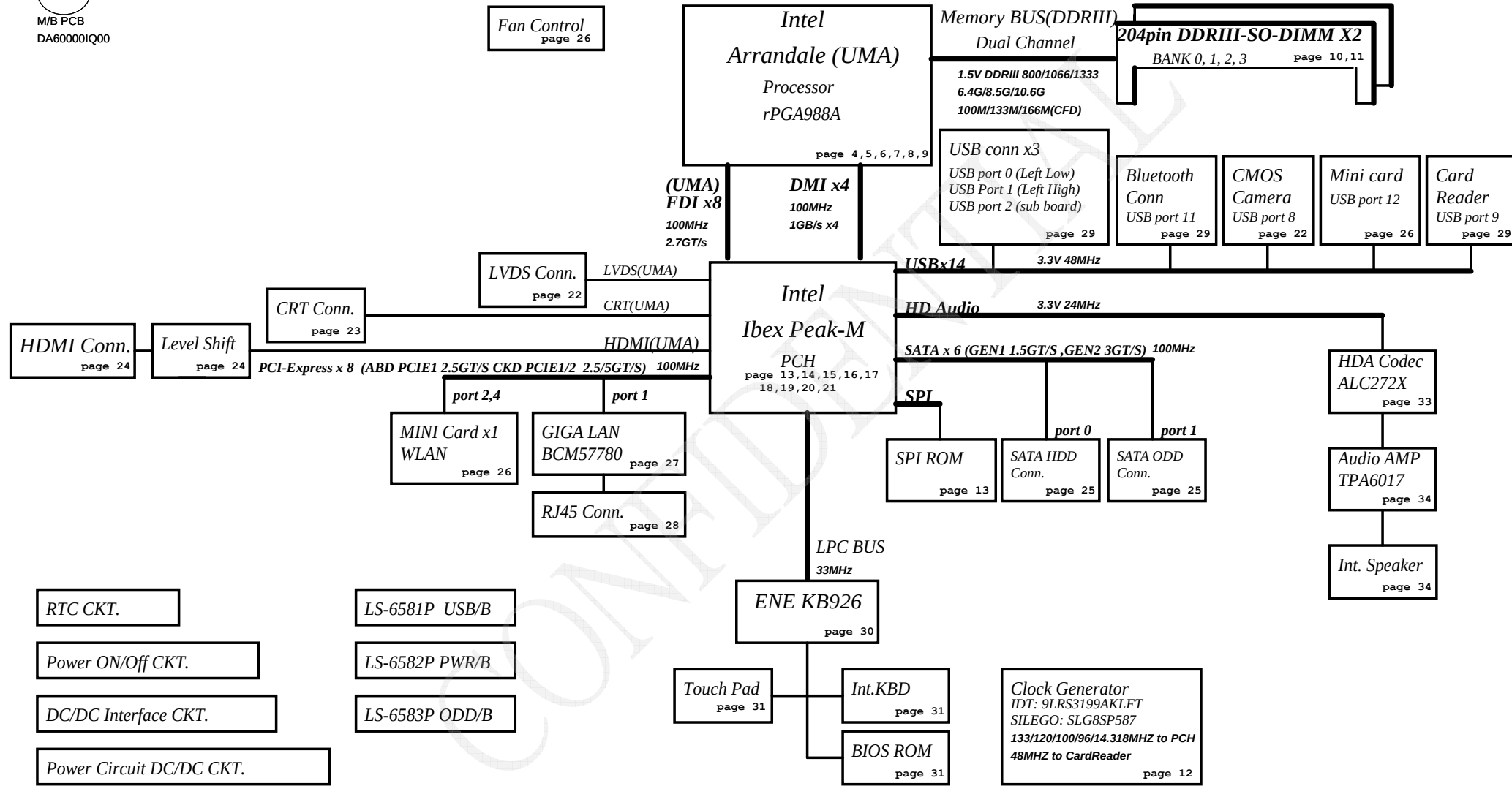
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/5/12	Deciphered Date	2010/04/15	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev A	Document Number 401939
Date: Friday, July 02, 2010				Sheet 1 of 48	

Compal Confidential

Model Name PEW71_81 UMA

File Name : LA-6582P

ZZZ1
M/B PCB
DA60000IQ00



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/5/12	Deciphered Date	2010/04/15	Title	Schematic, MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date	Friday, July 02, 2010
				Sheet	2 of 48

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	ON	OFF
+0.75VS	0.75V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail for PCH	ON	OFF	OFF
+1.05VS_VTT	1.05V switched power rail (1.05 for AUB CPU)	ON	OFF	OFF
+1.5V	1.5V power rail for DDRIII	ON	ON	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V_LAN	3.3V power rail for LAN	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

External PCI Devices

EC SM Bus1 address

EC SM Bus2 address

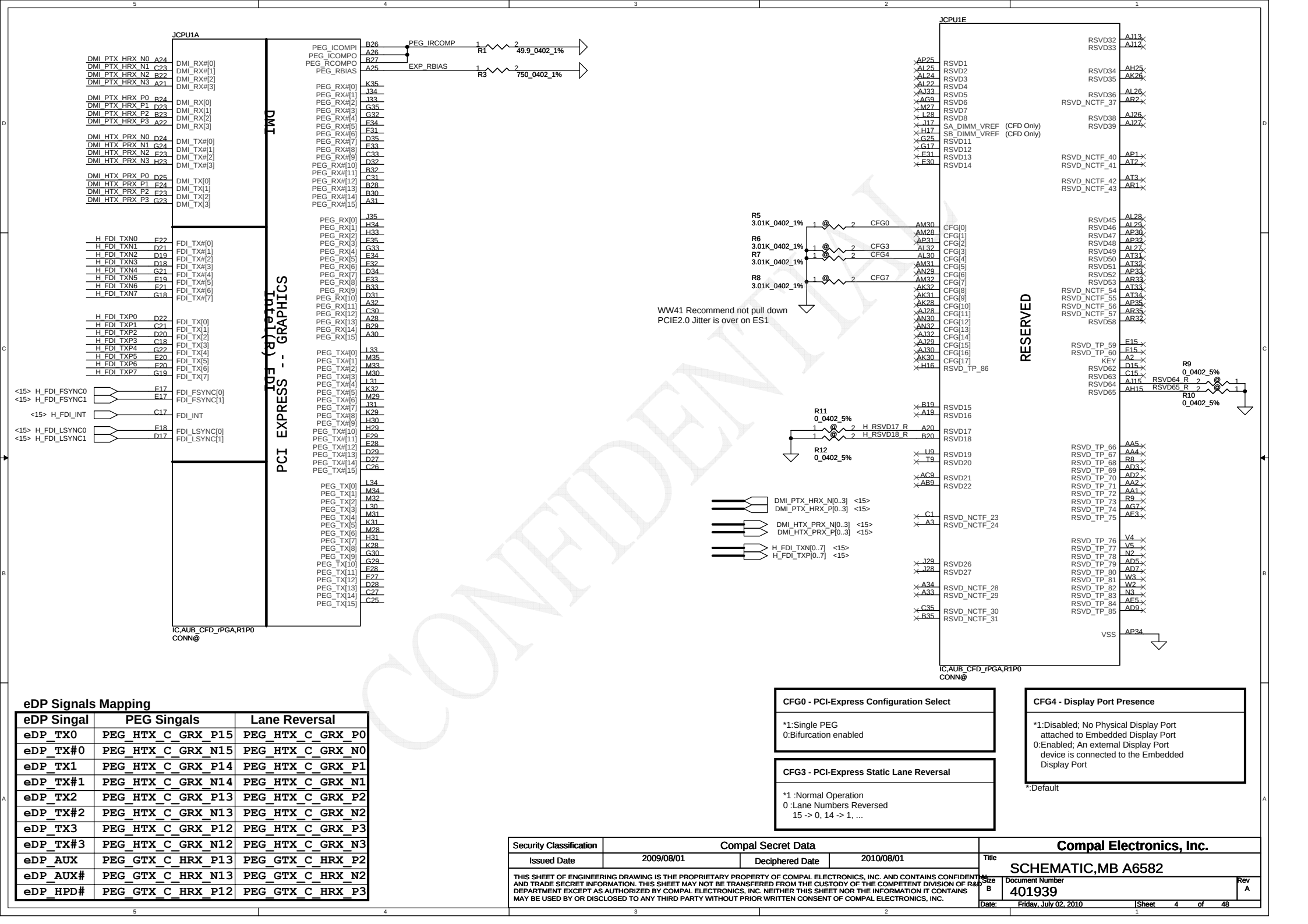
Device	Address
Clock Generator (9LRS3199AKLFT, SLG8SP587)	1101 0010b
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

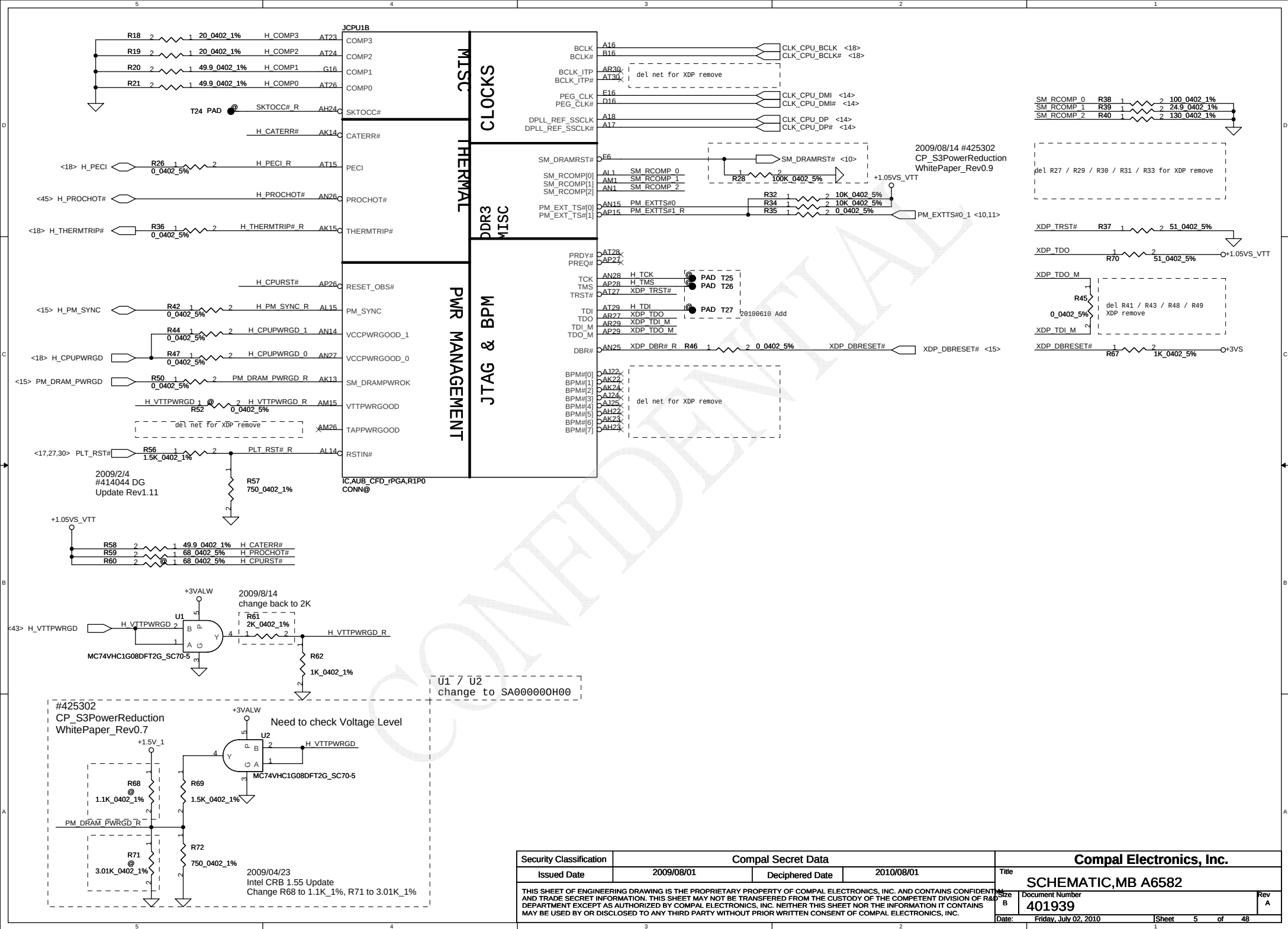
Project ID / Board ID Table for EC-AD channel

BTO Option Table

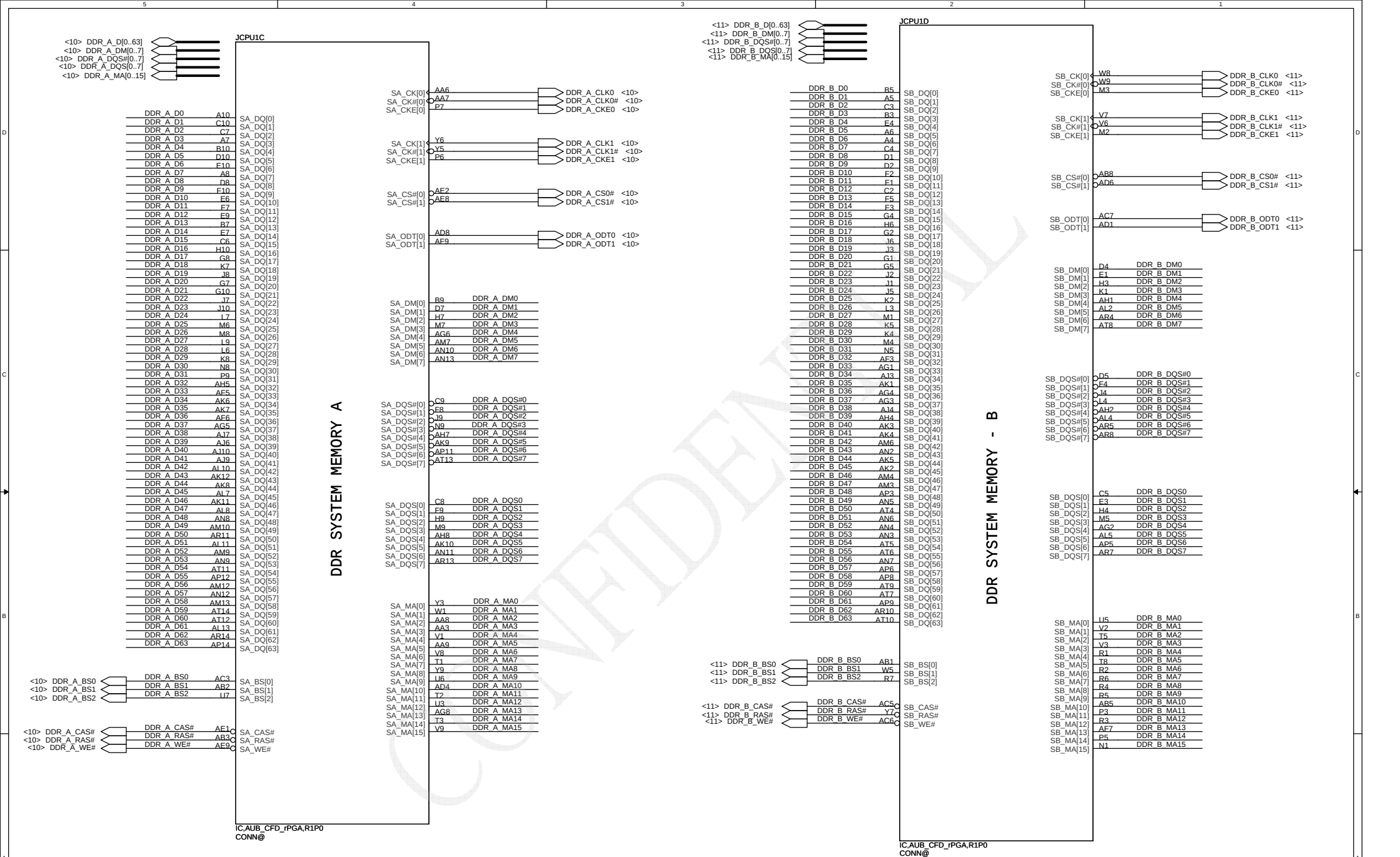
USB Port Table

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title	SCHEMATIC, MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc Number	401939	
				Rev	A	
				Date	Friday, July 02, 2010	
				ISheet	3 of 48	

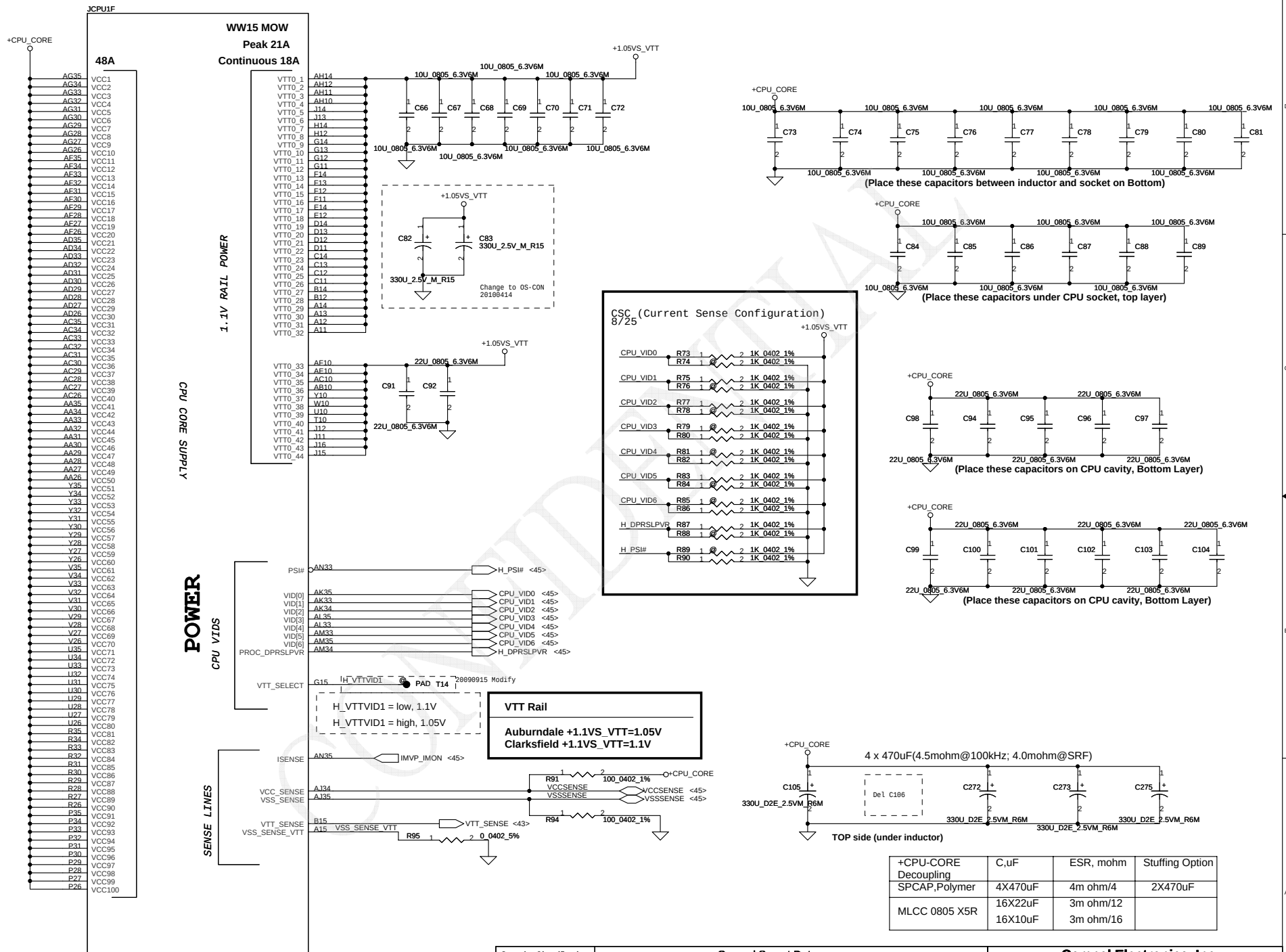


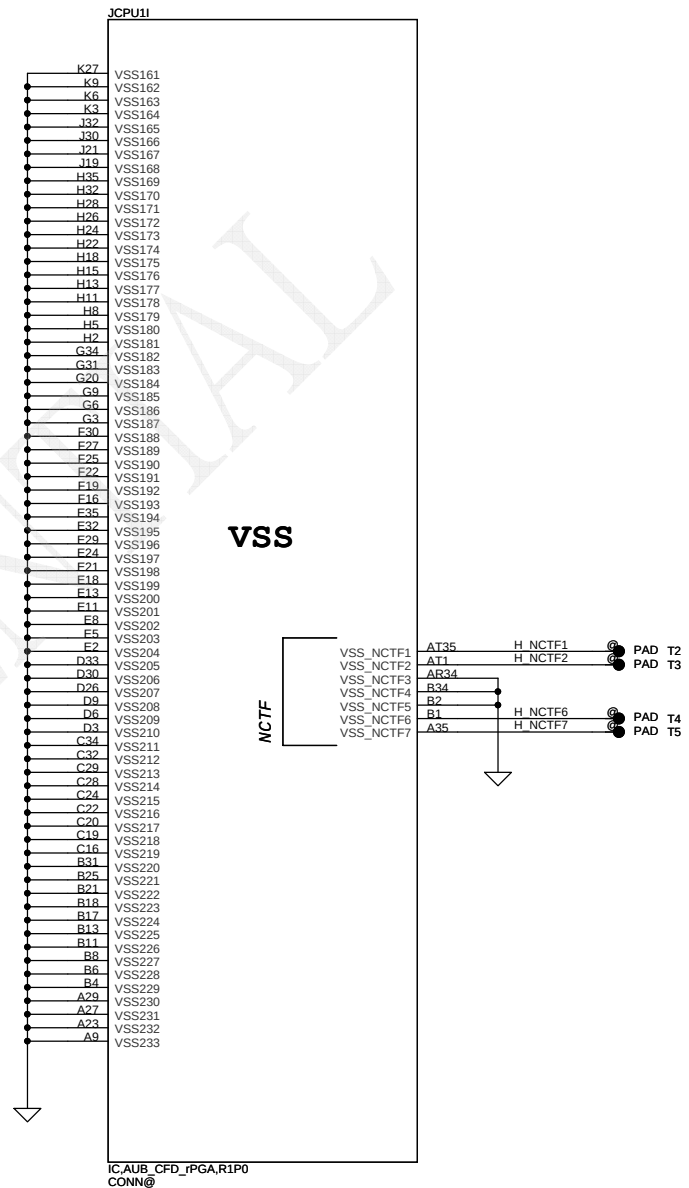
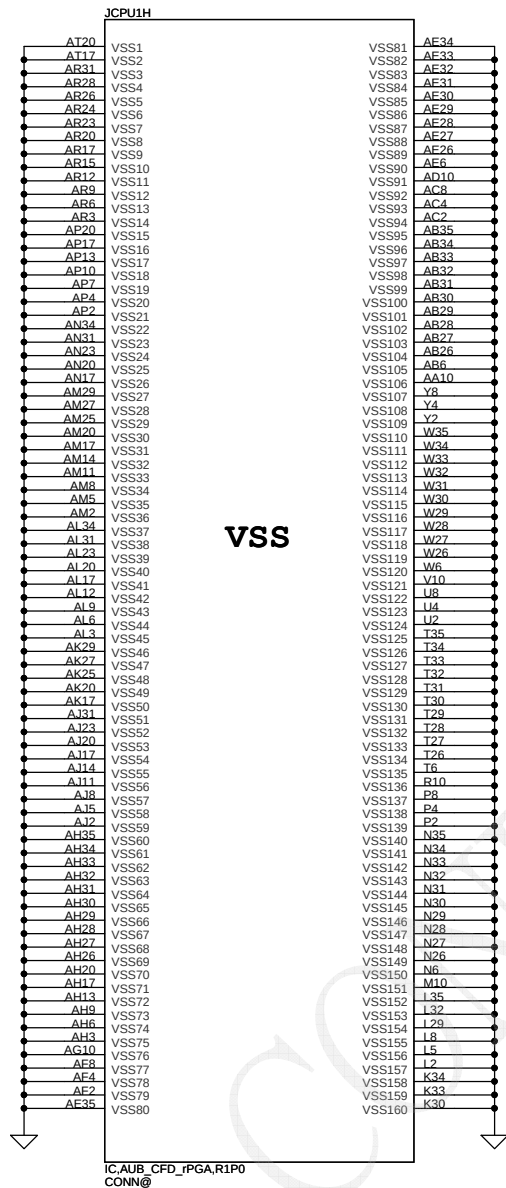


Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2009/08/01		Deciphered Date		2010/08/01	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. IT MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OR R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title			
				SCHEMATIC, MB A6582			
				Size B Document Number 401939		Rev A	
Date:		Friday, July 02, 2010		Sheet		5 of 48	

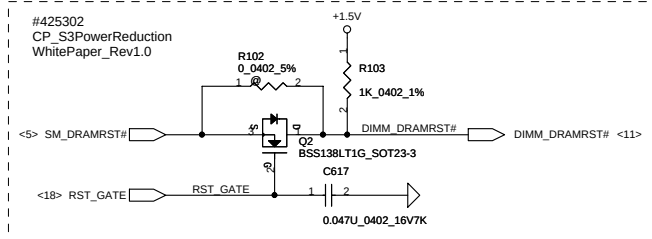
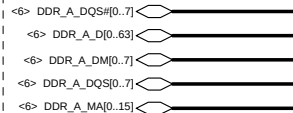


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT IT CONTAINS DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
Date:	Friday, July 02, 2010	Sheet	6	of	48





Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2009/08/01		Deciphered Date		2010/08/01		Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						SCHEMATIC,MB A6582					
						Size		Document Number		Rev A	
						Custom		401939			
Date:						Friday, July 02, 2010		Sheet 9 of 48			



Layout Note: Place these 4 Caps near Command and Control signals of DIMMA

+15V

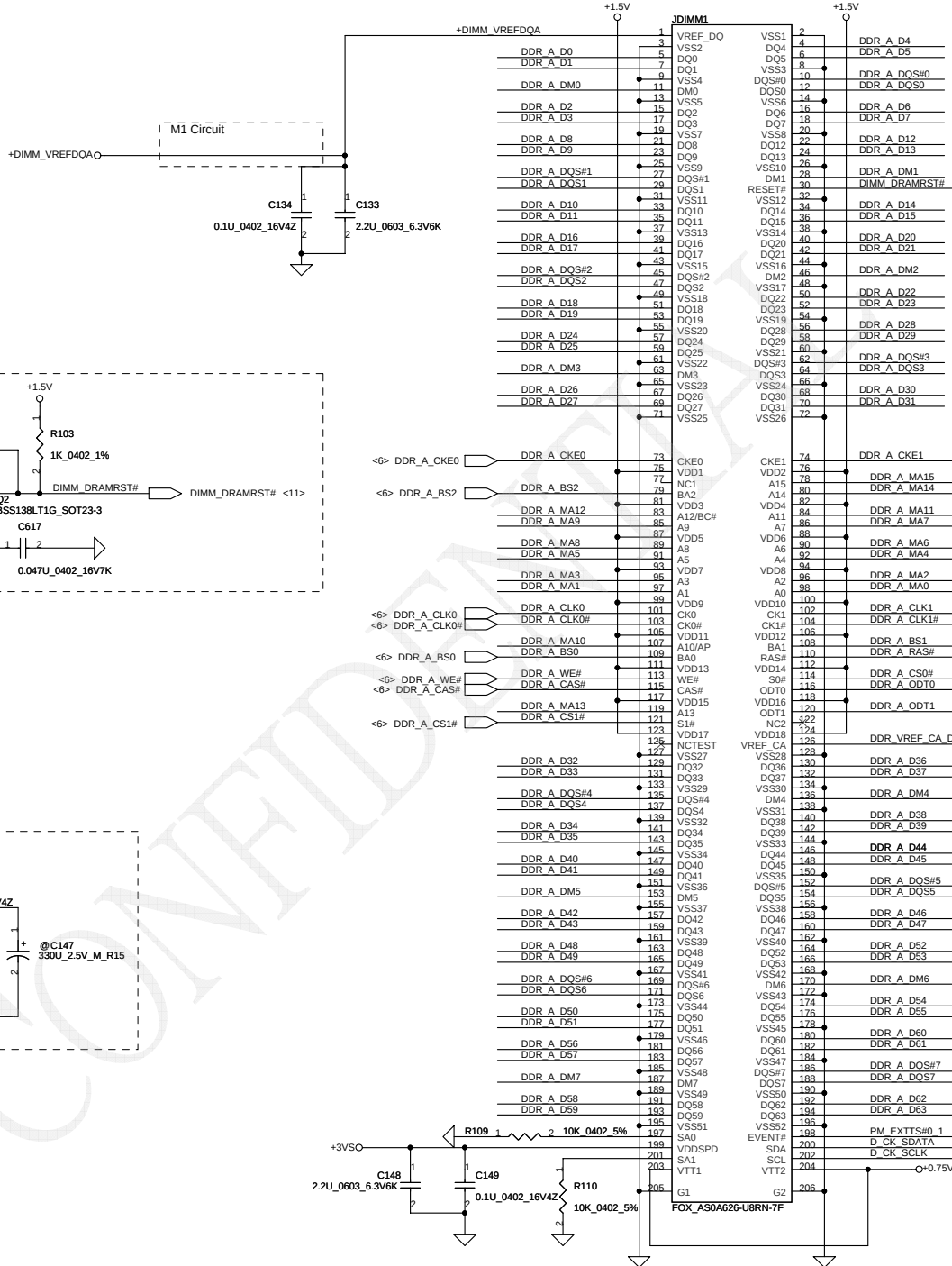
10U 0805 6.3V6M 10U 0805 6.3V6M 10U 0805 6.3V6M 0.1U 0402 16V4Z 0.1U 0402 16V4Z

C137 C138 C139 C140 C141 C142 C143 C144 C145 C146

1 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2

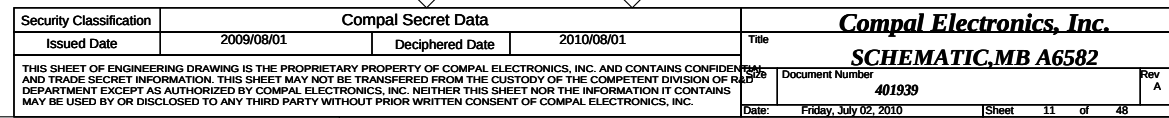
10U_0805_6.3V6M 10U_0805_6.3V6M 10U_0805_6.3V6M 0.1U_0402_16V4Z 0.1U_0402_16V4Z

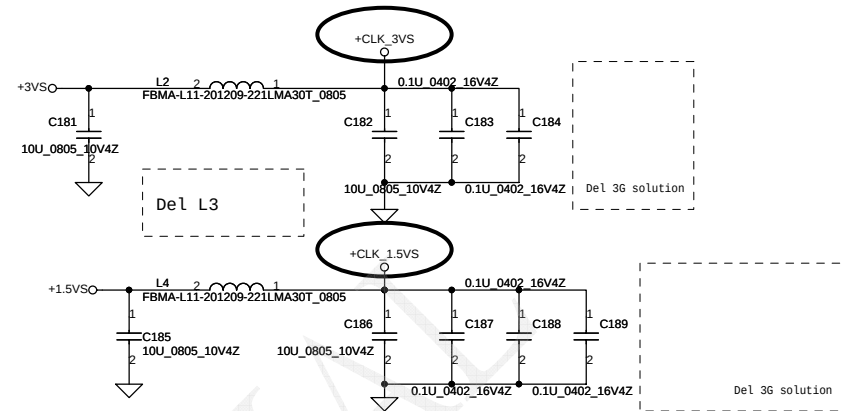
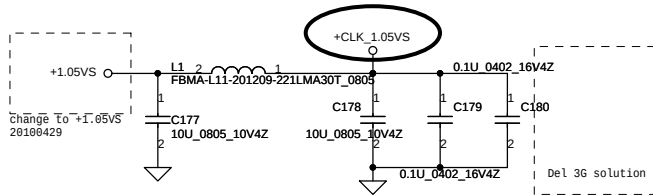
@C147 330U_2.5V_M_R15



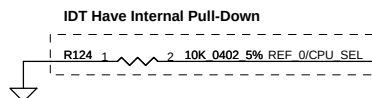
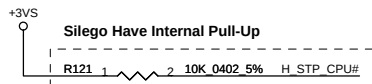
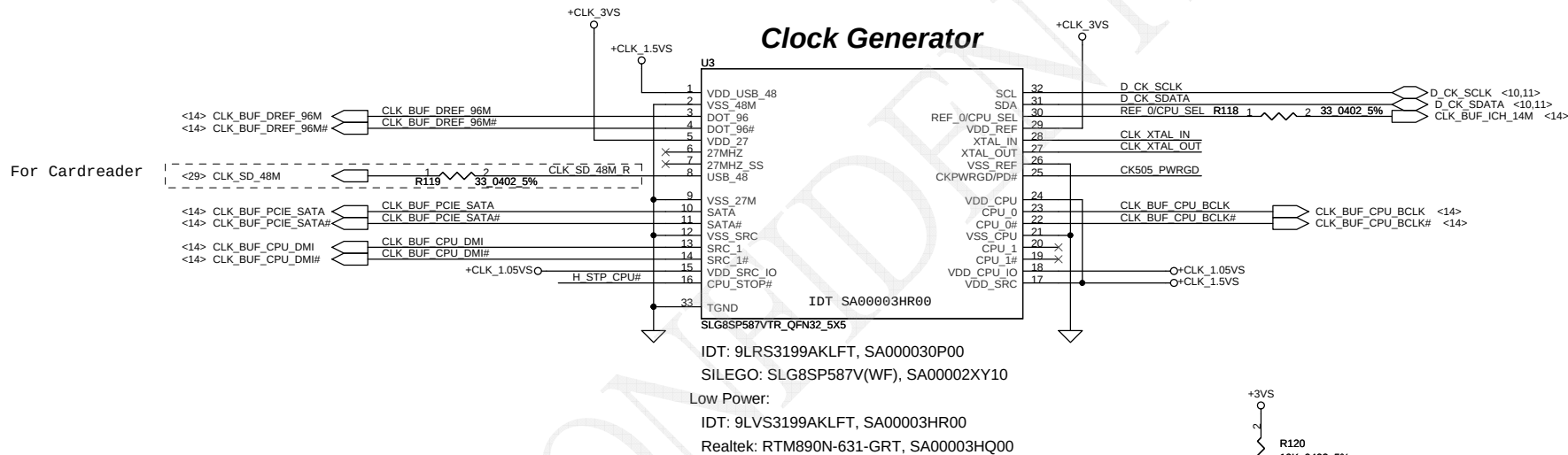
DDR3 SO-DIMM A Change to Reverse Type 8mm High

Security Classification	Compal Secret Data			Compal Electronics, Inc. SCHEMATIC, MB A6582		
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number 401339	Rev	A
Date:				Friday, July 02, 2010	Sheet	10 of 48

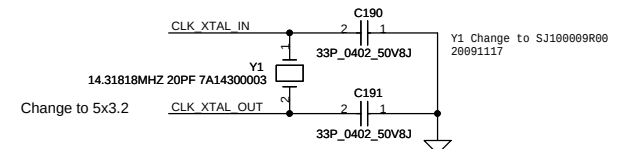
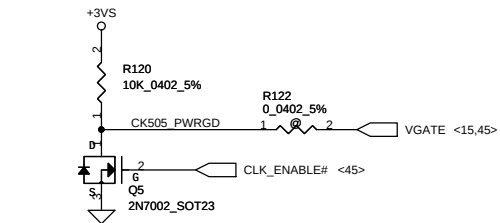
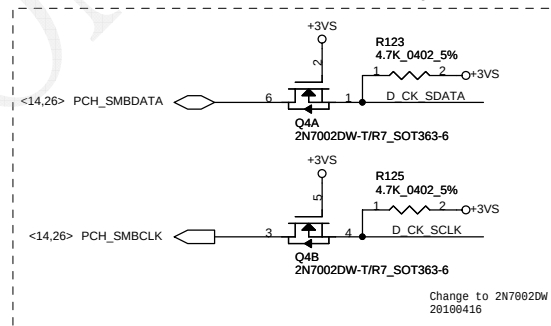




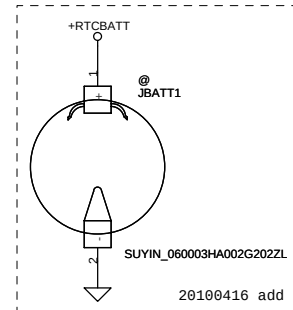
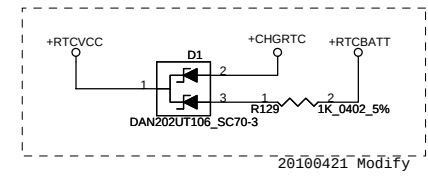
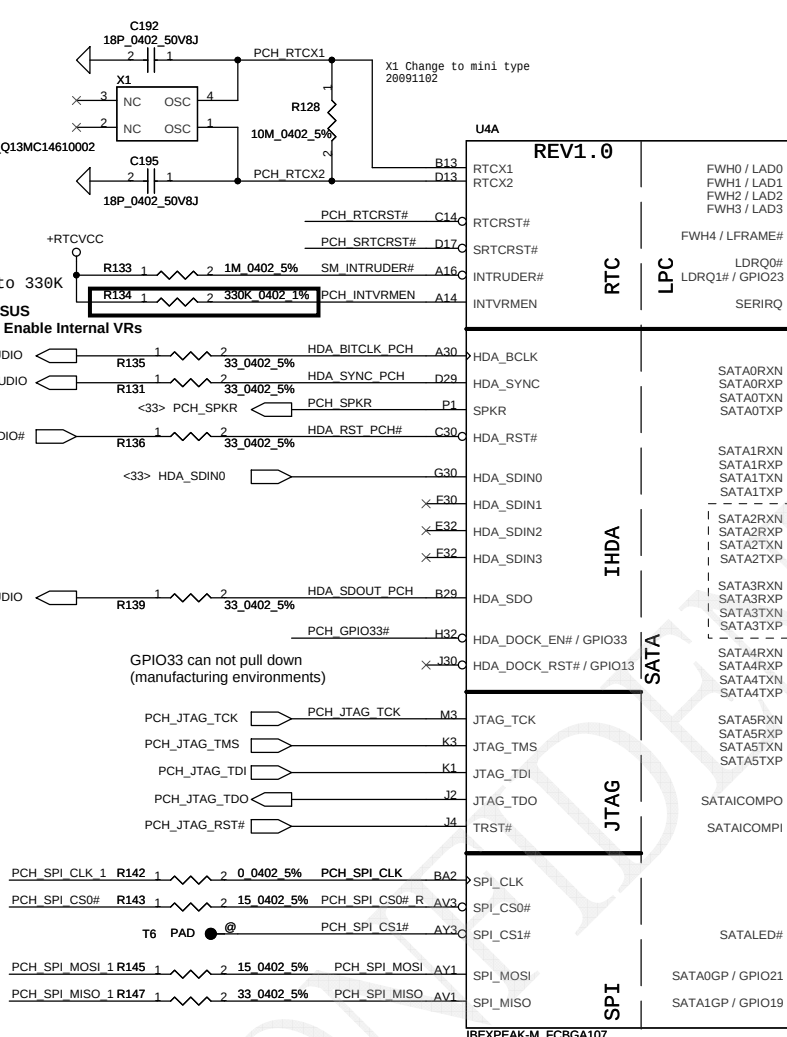
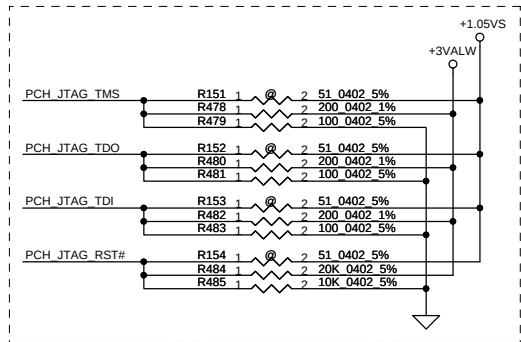
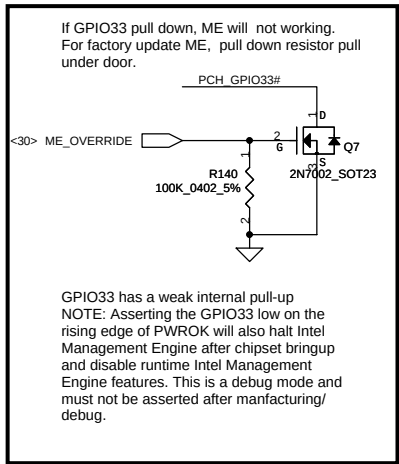
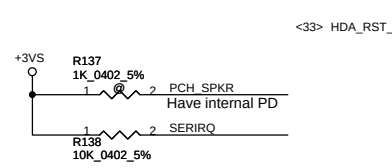
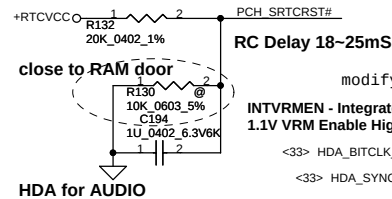
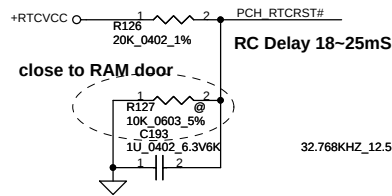
Clock Generator



PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz

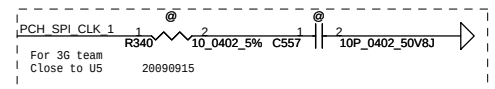
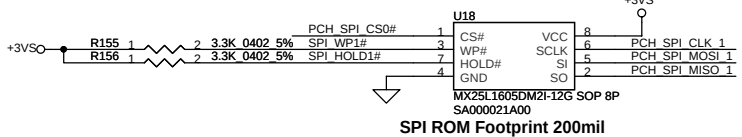
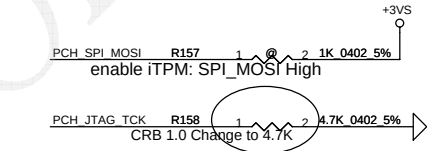


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title	SCHEMATIC, MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF REALTEK CORPORATION EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date	Friday, July 02, 2010
				Sheet	12 of 48



GPIO21	Project
0	NEW50/70/80/90
1	NEW71/91

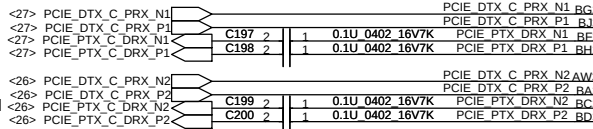
	GPIO19	GPIO37
	PCH_GPIO19	VGA_PRSTNT_L#
dGPU	0	0
iGPU	0	1
SG	1	X



Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date		2009/08/01		Deciphered Date		2010/08/01		Title		SCHEMATIC,MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Date		Friday, July 02, 2010	
								Sheet		13 of 48	

For PCIE LAN

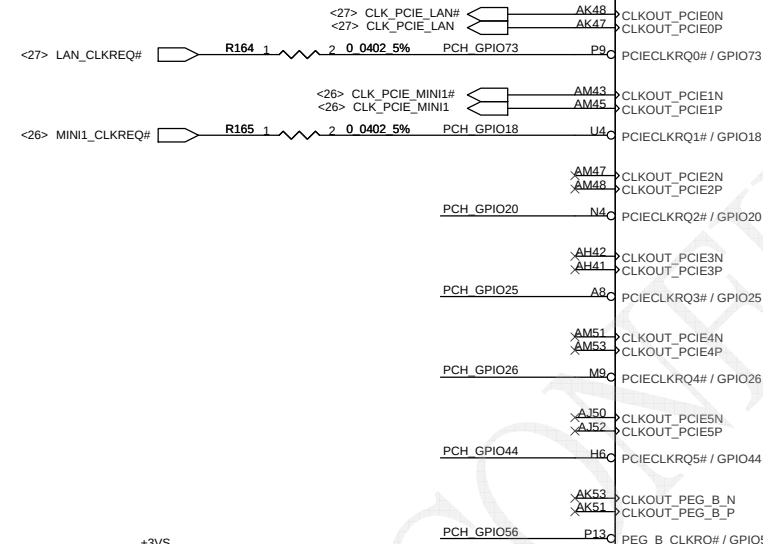
For Wireless LAN



2/10 PCIE7, PCIE8 not support on HM55

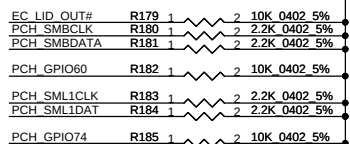
For PCIE LAN

For Wireless LAN



+3VS

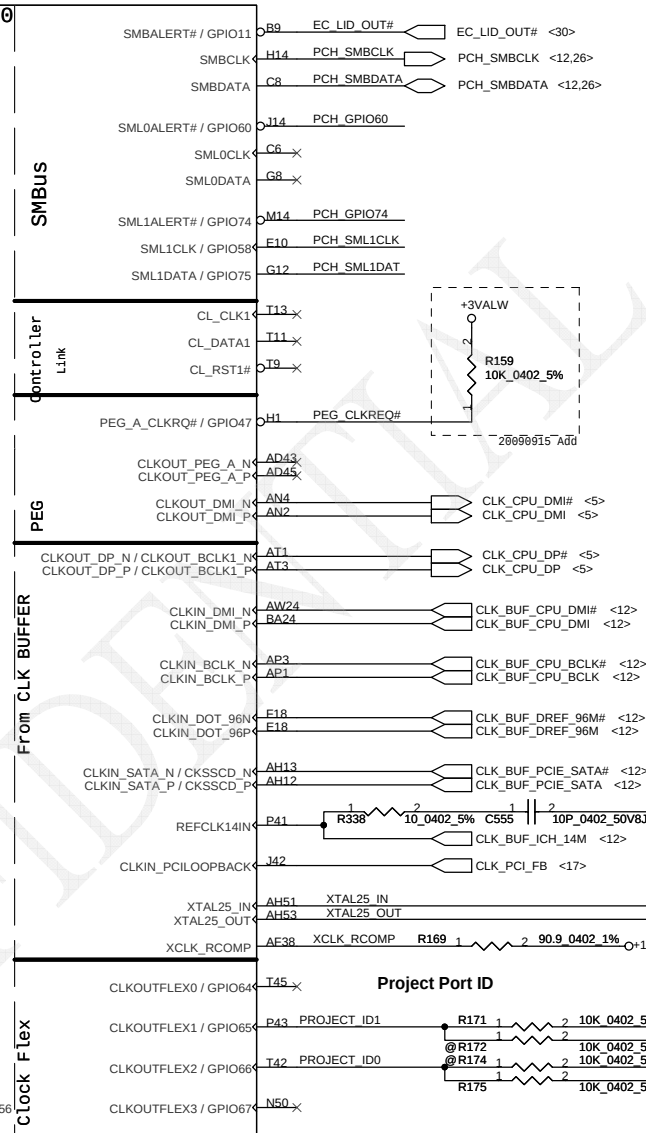
+3VALW



U4B

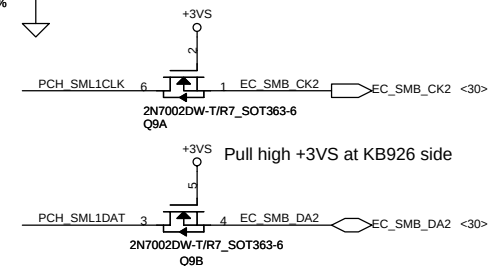
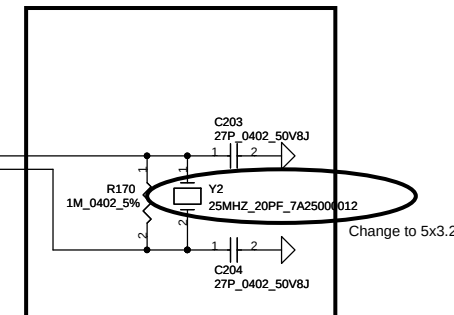
REV1.0

PCI-E*



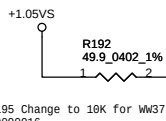
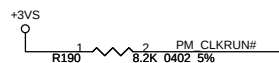
1. Connect Directly EXPRESS CARD, MINI1, MINI2
2. Level Shift1, Pull-Up to +3VS
CLOCK GEN, DIMM1, DIMM2
3. Level Shift2, Pull-Up to +3VS
LAN
4. Level Shift3, Pull-Up to +3VS
CPU & PCH XDP

6/9 MOW23 Request add 25MHz crystal supporting Integrated Graphics



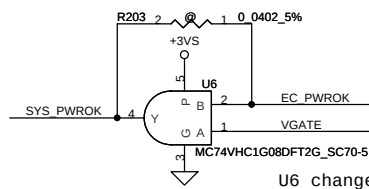
Security Classification		Compal Secret Data				Compal Electronics, Inc.			
Issued Date		2009/08/01		Deciphered Date		2010/08/01		Title	
								SCHEMATIC,MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.									
Size		Document Number		Rev					
Custom		401939		A					
Date:		Friday, July 02, 2010				Sheet		14 of 48	

<4> DMI_HTX_PRX_N[0..3] DMI HTX PRX N[0..3]
<4> DMI_HTX_PRX_P[0..3] DMI HTX PRX P[0..3]
<4> DMI_PTX_HRX_N[0..3] DMI PTX HRX N[0..3]
<4> DMI_PTX_HRX_P[0..3] DMI PTX HRX P[0..3]



R195 change to 10K for Wm37
20090916

10/2
R199 Intel suggestion change to 10K



No used Integrated LAN,
connecting LAN_RST# to GND

U4C
DMI HTX PRX N0 BC24
DMI HTX PRX N1 BJ22
DMI HTX PRX N2 AW20
DMI HTX PRX N3 BJ20
DMI HTX PRX P0 BD24
DMI HTX PRX P1 BC22
DMI HTX PRX P2 BA20
DMI HTX PRX P3 BG20
DMI PTX HRX N0 BE22
DMI PTX HRX N1 BE21
DMI PTX HRX N2 BD20
DMI PTX HRX N3 BE18
DMI PTX HRX P0 BD22
DMI PTX HRX P1 BH21
DMI PTX HRX P2 BC20
DMI PTX HRX P3 BD18

DMIORXN
DMI1RXN
DMI2RXN
DMI3RXN
DMIORXP
DMI1RXP
DMI2RXP
DMI3RXP
DMIOTXN
DMI1TXN
DMI2TXN
DMI3TXN
DMIOTXP
DMI1TXP
DMI2TXP
DMI3TXP

REV1.0

System Power Management

SYS_RESET#
WAKE#
CLKRUN# / GPIO32
PCH_GPIO61
SUS_STAT# / GPIO61
SUSCLK / GPIO62
SLP_S5# / GPIO63
SLP_S4#
SLP_S3#
SLP_M#
TP23
PMSYNCH
SLP_LAN# / GPIO29

IBEXPEAK-M_FCBGA107

DMI

FDI

FDI_RXN0
FDI_RXN1
FDI_RXN2
FDI_RXN3
FDI_RXN4
FDI_RXN5
FDI_RXN6
FDI_RXN7
FDI_RXP0
FDI_RXP1
FDI_RXP2
FDI_RXP3
FDI_RXP4
FDI_RXP5
FDI_RXP6
FDI_RXP7

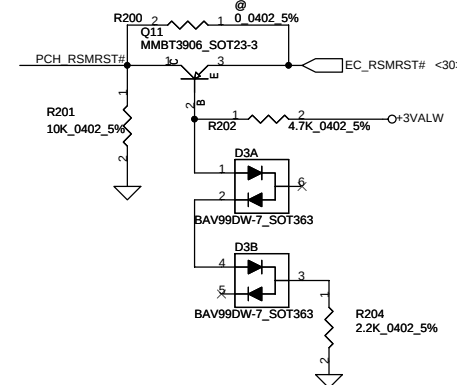
FDI_INT
FDI_FSYNC0
FDI_FSYNC1
FDI_LSYNC0
FDI_LSYNC1

H_FDI_TXN0
H_FDI_TXN1
H_FDI_TXN2
H_FDI_TXN3
H_FDI_TXN4
H_FDI_TXN5
H_FDI_TXN6
H_FDI_TXN7
H_FDI_TXP0
H_FDI_TXP1
H_FDI_TXP2
H_FDI_TXP3
H_FDI_TXP4
H_FDI_TXP5
H_FDI_TXP6
H_FDI_TXP7

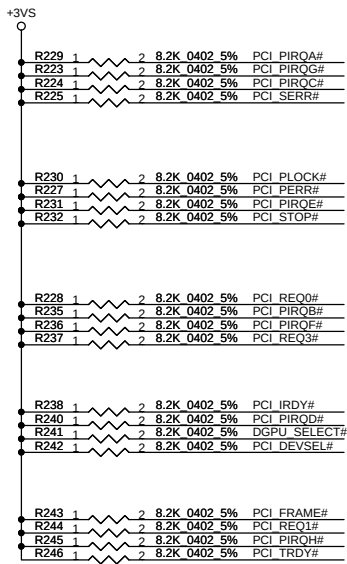
H_FDI_INT <4>
H_FDI_FSYNC0 <4>
H_FDI_FSYNC1 <4>
H_FDI_LSYNC0 <4>
H_FDI_LSYNC1 <4>

H_FDI_TXN[0..7] H_FDI_TXN[0..7] <4>
H_FDI_TXP[0..7] H_FDI_TXP[0..7] <4>

PCH_PCIE_WAKE# <26,27>
PM_CLKRUN# <30>
PCH_GPIO61 @ PAD T7
PCH_SUSCLK <30>
PM_SLP_S5# <30>
PM_SLP_S4# <30>
PM_SLP_S3# <30>
PM_SLP_M# @ PAD T9
PM_SLP_DSW# @ PAD T10
H_PM_SYNC <5>



Security Classification		Compal Secret Data				Compal Electronics, Inc.							
Issued Date		2009/08/01		Deciphered Date		2010/08/01		Title		SCHEMATIC,MB A6582			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Size		Document Number		Rev			
						Custom		401939		A			
						Date		Friday, July 02, 2010		Sheet		15 of 48	



A16 swap override Strap/Top-Block Swap Override jumper

Low=A16 swap override/Top-Block Swap Override enabled High=Default *

T11 PAD

T12 PAD

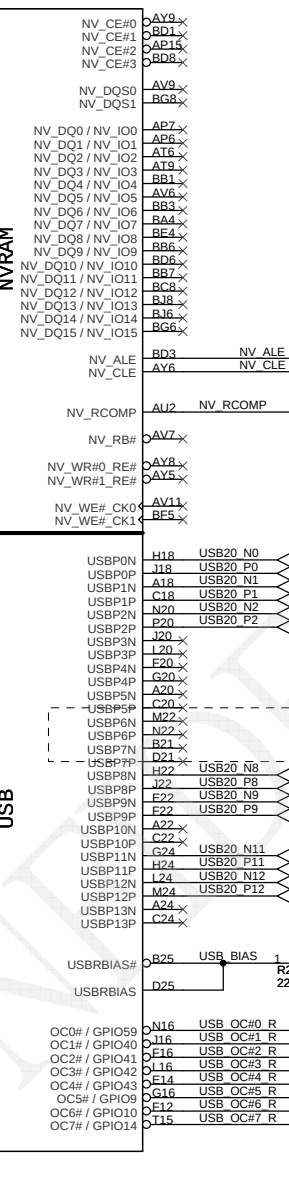
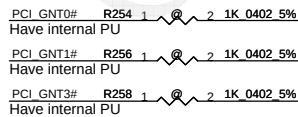
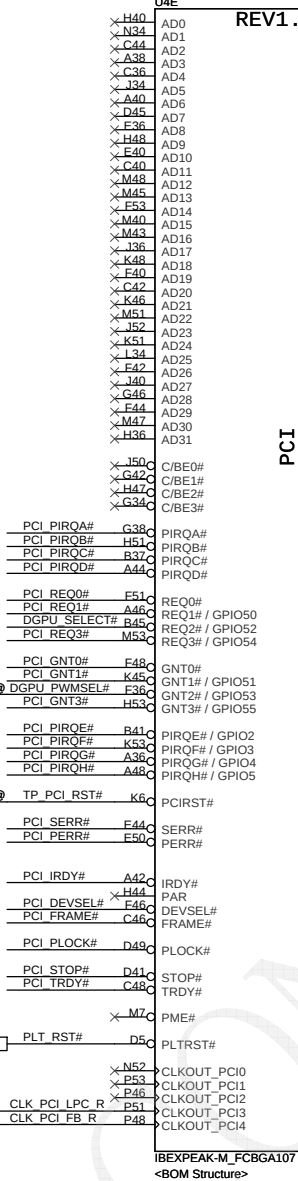
<30> CLK_PCI_LPC
<14> CLK_PCI_FB

2008/1/6 2009MOW01 change to 22 ohm

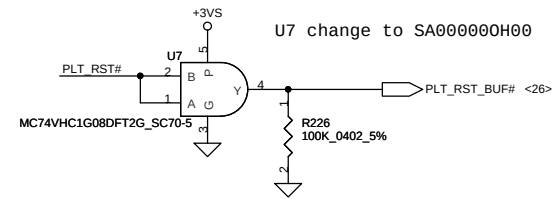
Boot BIOS Strap		
PCI_GNT#0	PCI_GNT#1	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

A16 swap override Strap/Top-Block Swap Override jumper

PCI_GNT#3 Low = A16 swap High = Default



OC[0..3] use for EHCI 1
OC[4..7] use for EHCI 2



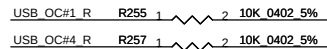
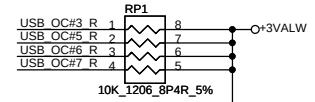
Intel Anti-Theft Techonlogy	
NV_ALE	High=Endabled
NV_CLE	Low=Disabale(floating)★

DMI Termination Voltage	
NV_ALE	Set to Vcc when HIGH
NV_CLE	Set to Vss when LOW

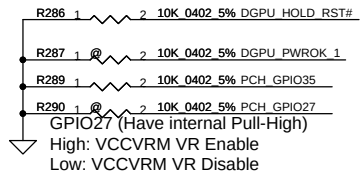
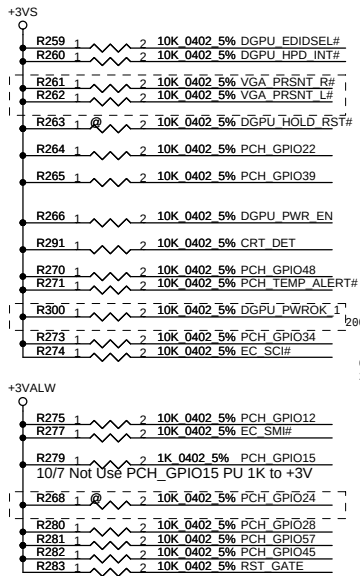
NV_ALE
Enable Intel Anti-Theft Technology : 8.2K PU to +3V5
Disable Intel Anti-Theft Technology : floating(internal PD)
NV_CLE
DMI termination voltage.
weak internal PU, don't PD

USB/B (Left Side Low)
USB Port (Left Side High)
USB/B (Right Side)

CMOS Camera (LVDS)
Card Reader
Del SIM Card USB
Bluetooth
Mini Card(WLAN)
Del 3G Card USB



Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date	2009/08/01		Deciphered Date	2010/08/01		Title	SCHEMATIC,MB A6582				
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Size	Document Number	Rev			
						Custom	401939	A			
						Date:	Friday, July 02, 2010	Sheet	17	of	48



	GPIO19	GPIO37
	PCH_GPIO19	VGA_PRSNT_L#
dGPU	0	0
iGPU	0	1
SG	1	0

GPIO27

On-Die PLL Voltage Regulator

This signal has a weak internal pull up

* H: On-Die voltage regulator enable

L: On-Die PLL Voltage Regulator disable

GPIO8

This signal has a weak internal pull up

can't Pull low

GPIO15

L: Intel ME Crypto Transport Layer Security(TLS) chiper suite with no confidentiality

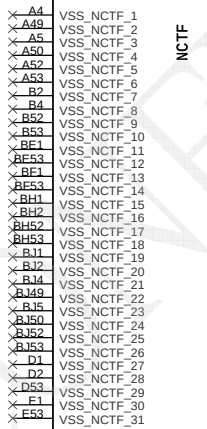
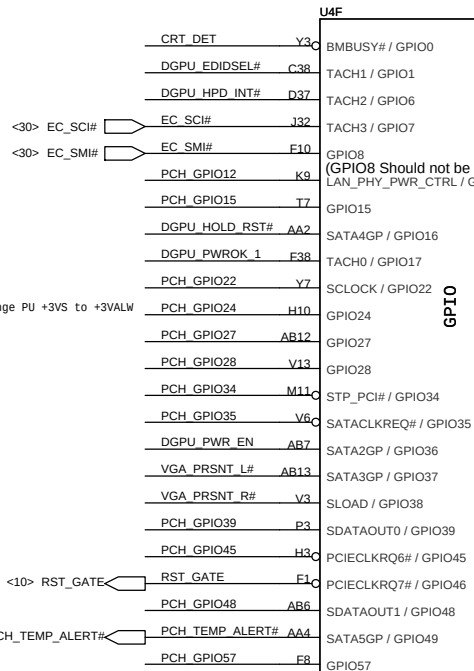
H: Intel ME Crypto Transport Layer Security(TLS) chiper suite with confidentiality

it have weak internal PU 20K

GPIO24 change PU +3VS to +3VALW

20090915 Add

20090916



IBEXPEAK-M_FCBGA107

REV1.0

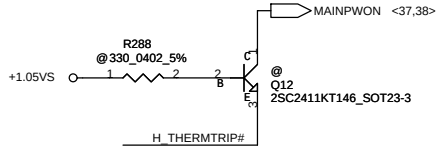
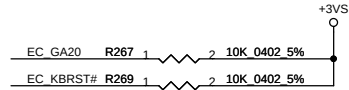
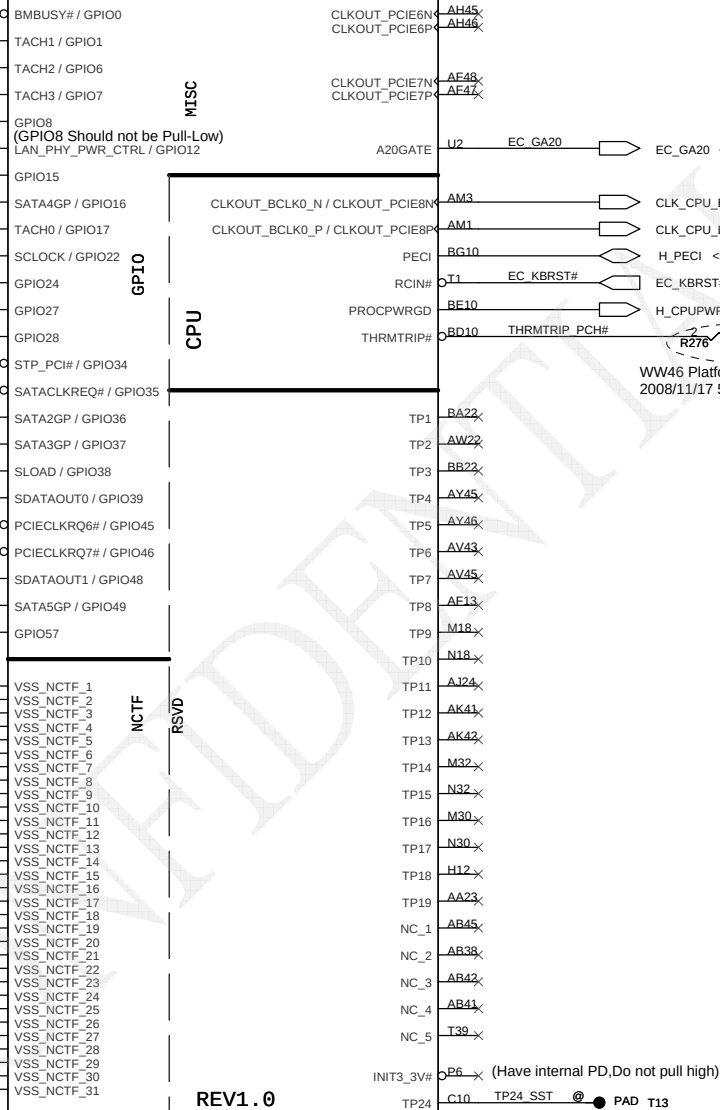
MTSC

GPIO

CPU

NCTF

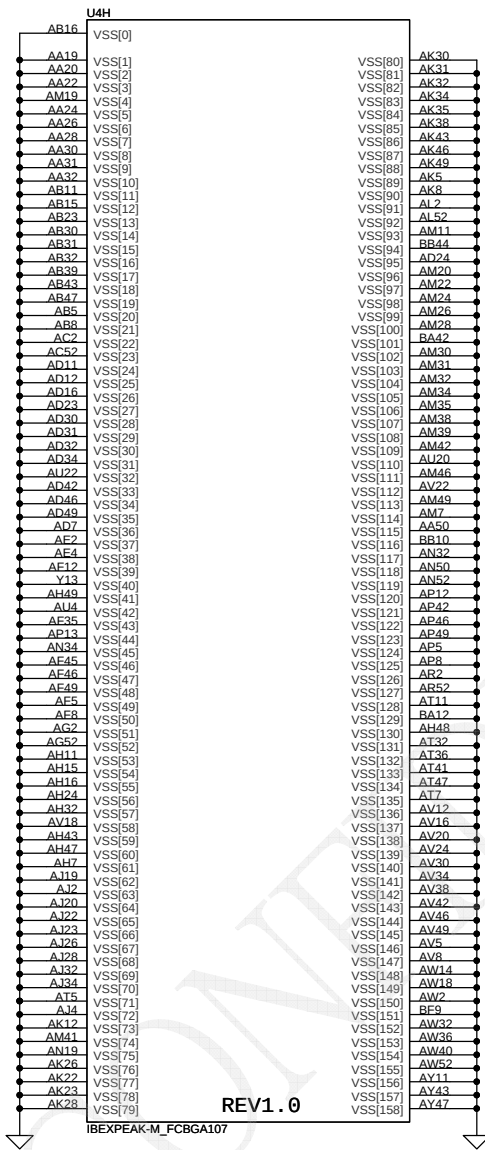
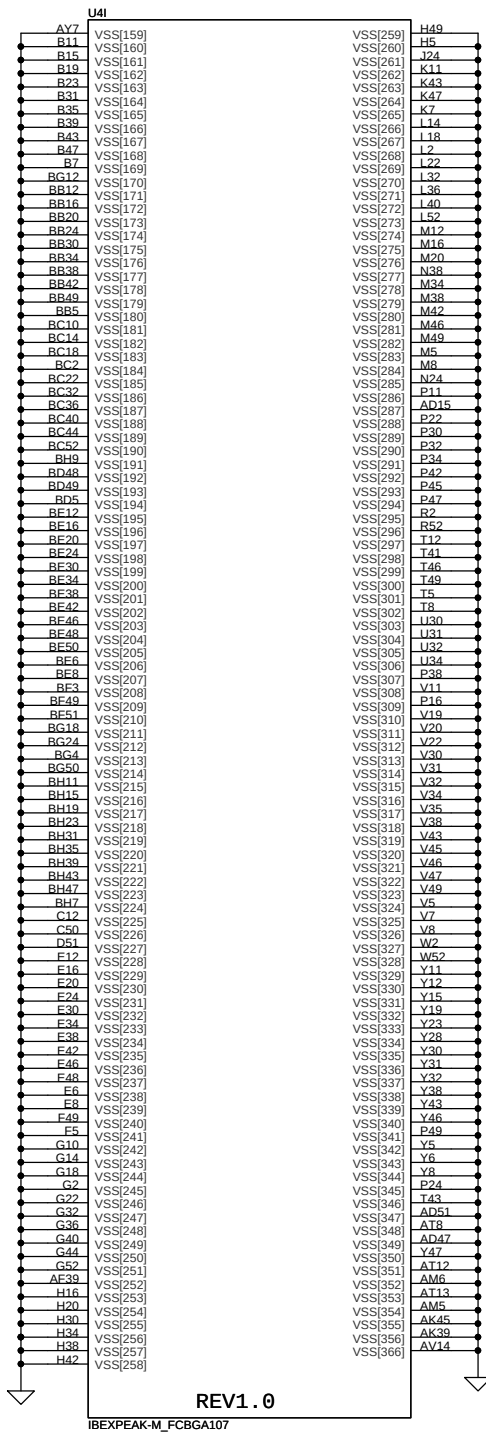
RSVD



INIT3_3V

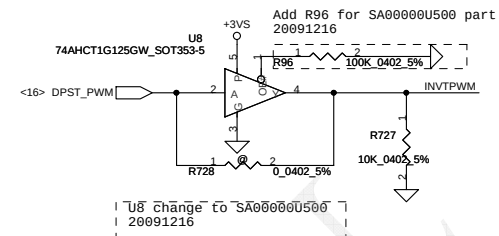
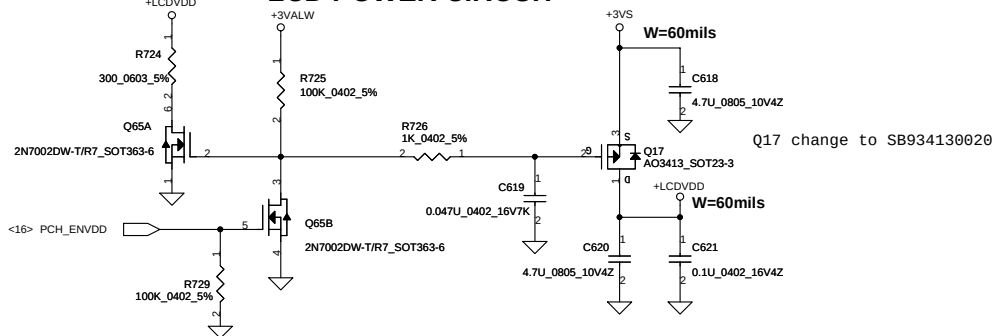
This signal has weak internal PU, can't pull low

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date	Friday, July 02, 2010
				Sheet	18 of 48

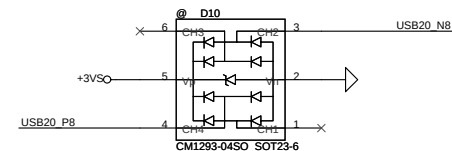
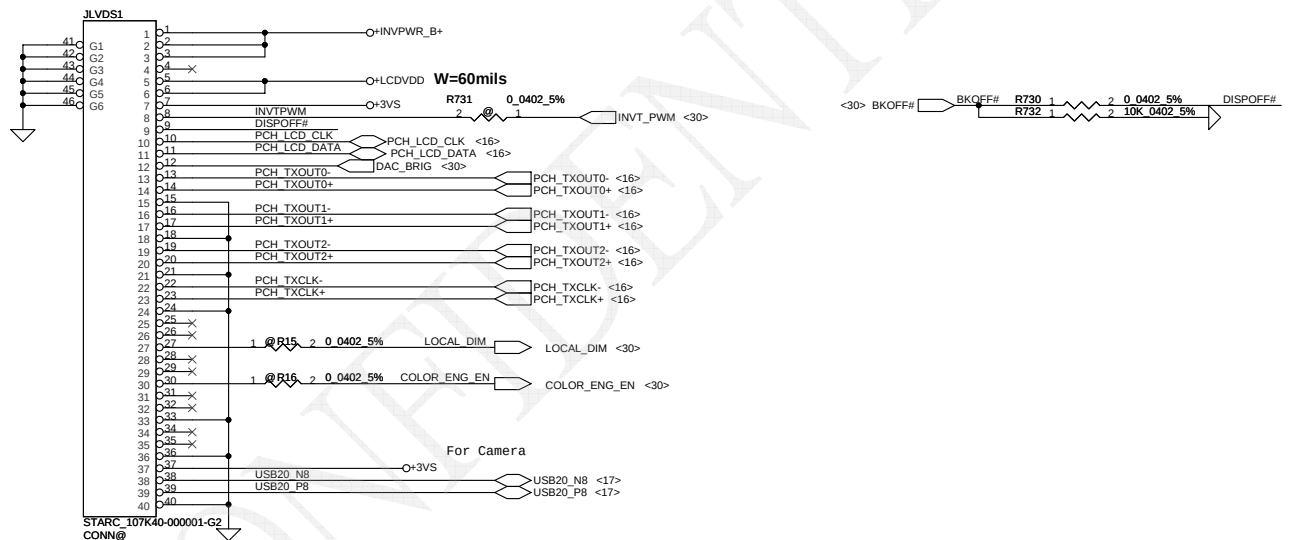
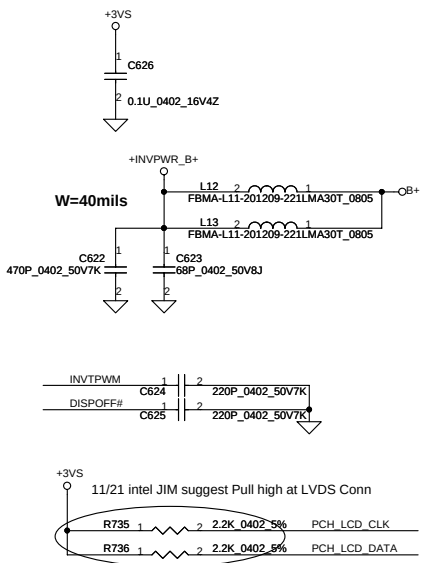


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/01	Deciphered Date	2010/08/01	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Rev	A
				Date:	Friday, July 02, 2010
				Sheet	21 of 48

LCD POWER CIRCUIT

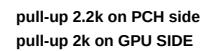


LED PANEL Conn.



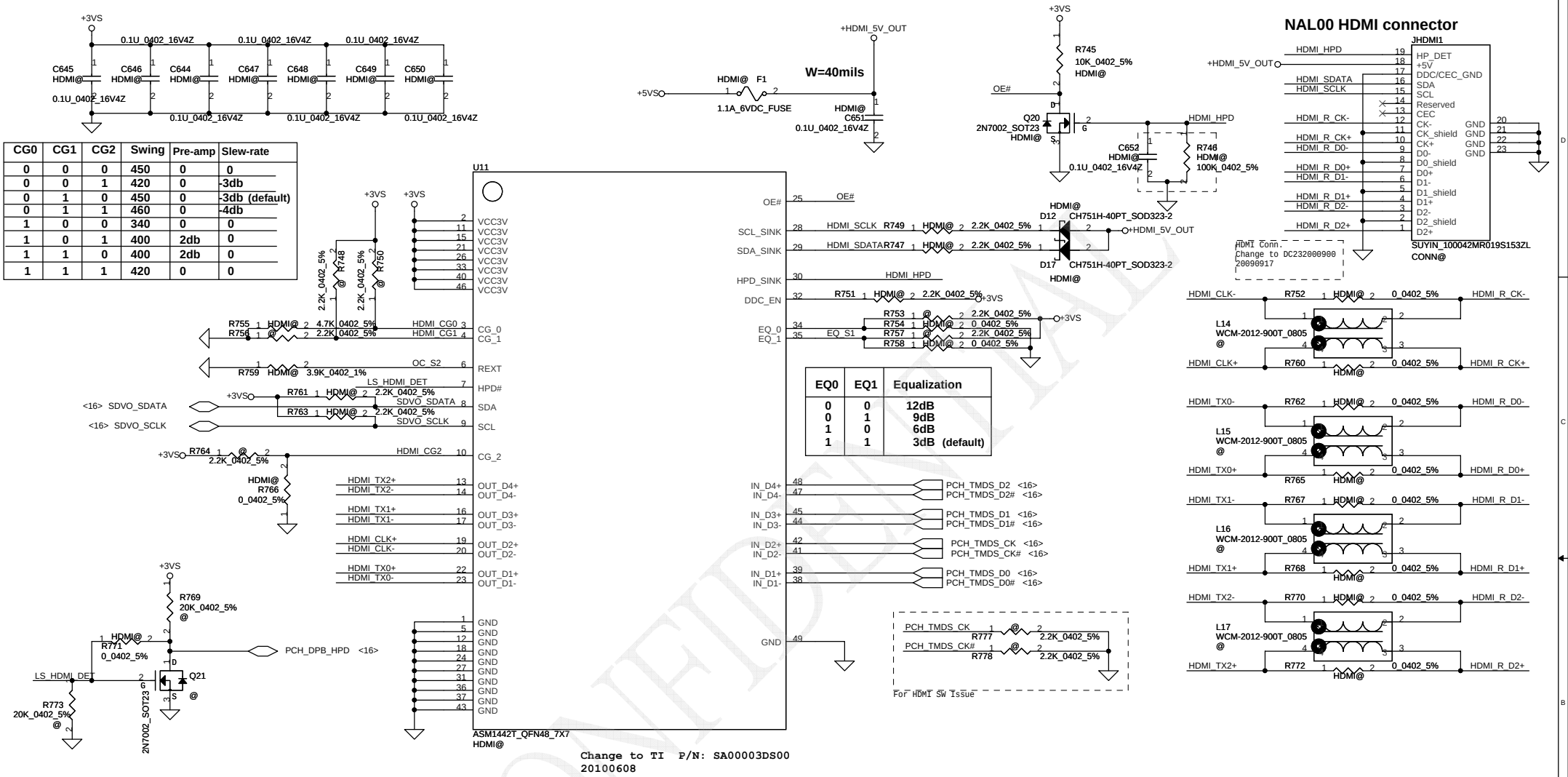
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/5/12	Deciphered Date	2010/04/15	Title	SCHEMATIC, MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date	Friday, July 02, 2010
				Sheet	22 of 48

D7 / D8 / D9
change to SC6BAV99390

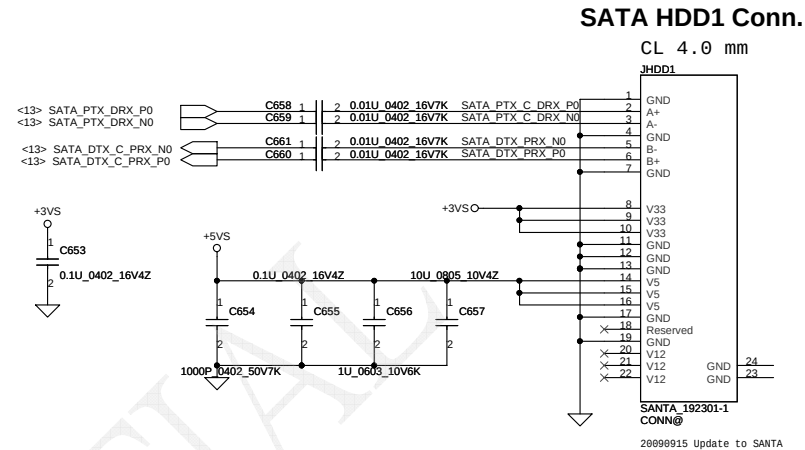
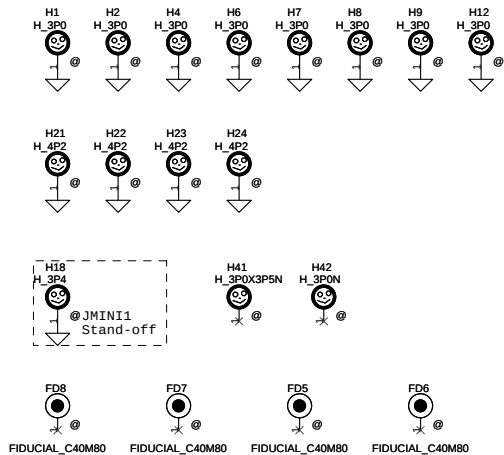


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2009/5/12	Deciphered Date	2010/04/15	Title	SCHEMATIC, MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc No	Document Number	Rev
				A	401939	A
				Date	Friday, July 02, 2010	Sheet 23 of 48

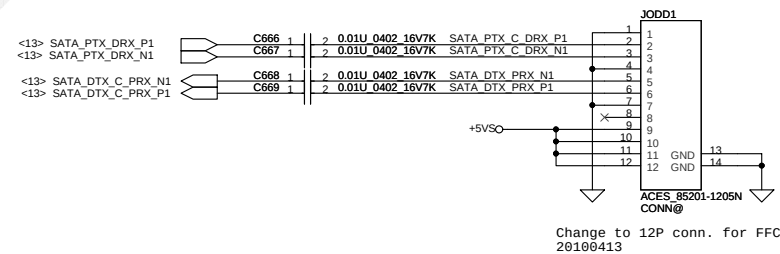
CG0	CG1	CG2	Swing	Pre-amp	Slew-rate
0	0	0	450	0	0
0	0	1	420	0	-3db
0	1	0	450	0	-3db (default)
0	1	1	460	0	-4db
1	0	0	340	0	0
1	0	1	400	2db	0
1	1	0	400	2db	0
1	1	1	420	0	0



Change to TI P/N: SA00003DS00
20100608

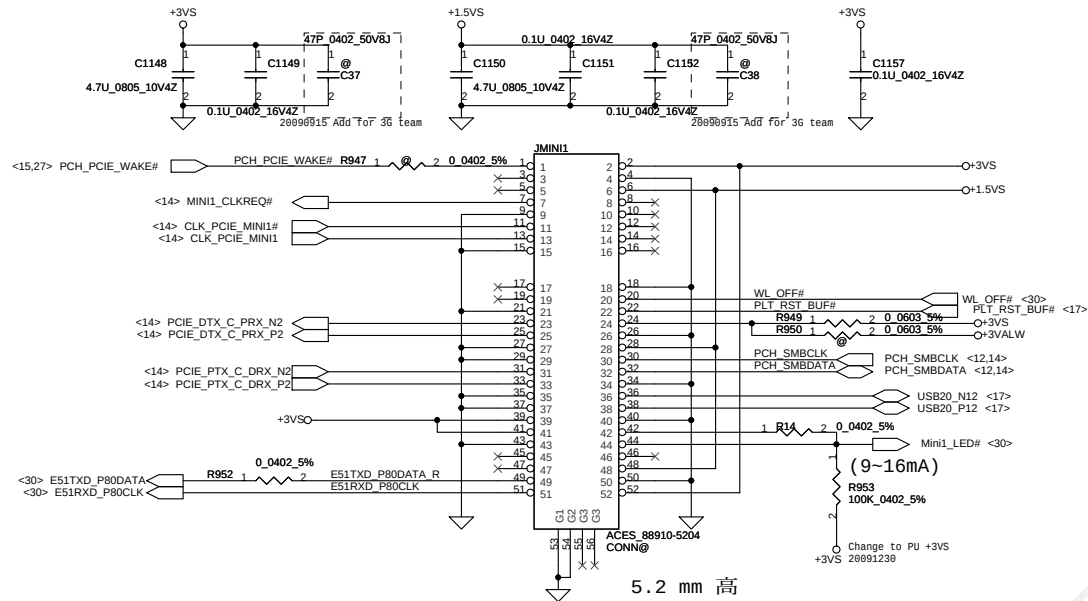


**SATA ODD
FFC Conn.**



Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2009/5/12		Deciphered Date		2010/04/15		Title		SCHEMATIC,MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Document Number		Rev	
								401939		A	
								Date:		Friday, July 02, 2010	
								Sheet		25 of 48	

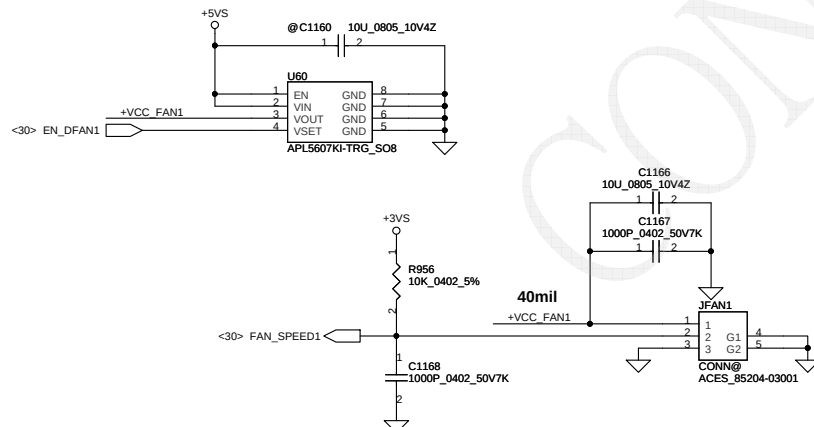
For Wireless LAN



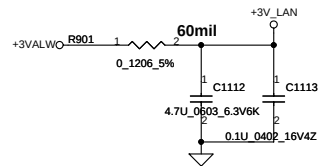
Del 3G / GPS Module Connect

Mini Card Power Rating			
Power	Primary Power (mA)		Auxiliary Power (mA)
	Peak	Normal	Normal
+3VS	1000	750	
+3V	330	250	250 (wake enable)
+1.5VS	500	375	5 (Not wake enable)

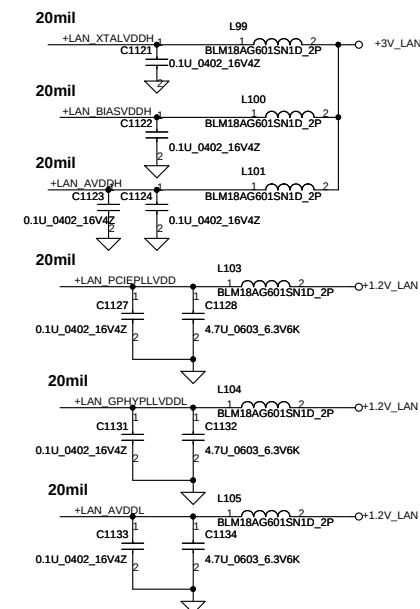
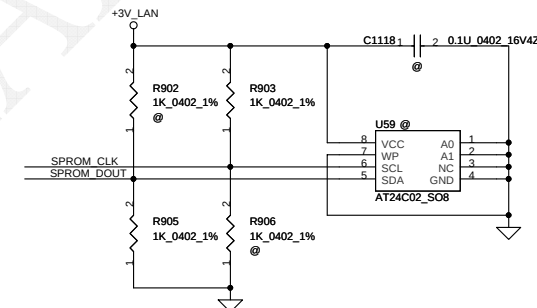
FAN1 Conn



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2009/5/12	Deciphered Date	2007/12/25	Title	SCHMATIC, MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date	Friday, July 02, 2010
				Sheet	26 of 48

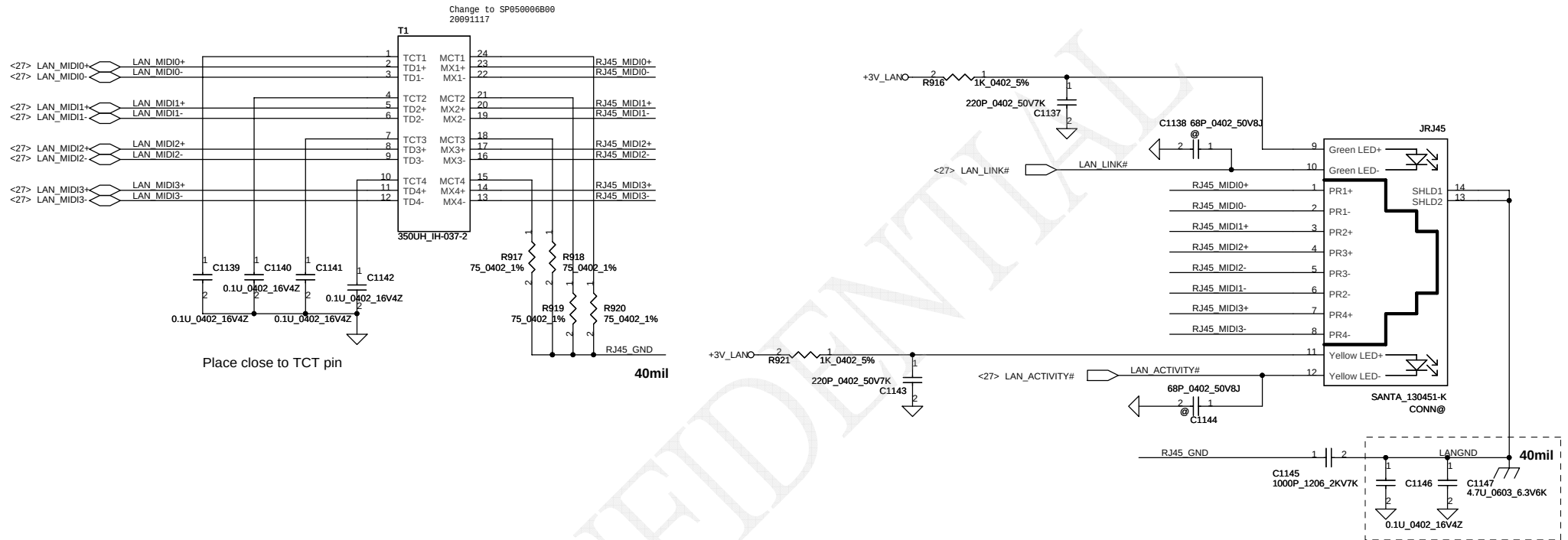


	SPROM_CLK (EECLK)	SPROM_DOUT (EEDATA)
On chip	1	0
AT24C02	1	1



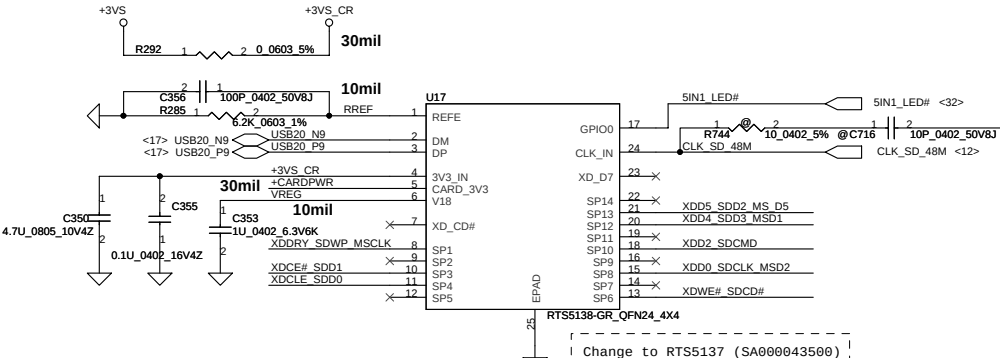
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/08/10	Deciphered Date	2009/08/10	Title	SCHMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc Number	Rev A
				Customer	401939
Date:	Friday, July 02, 2010	Sheet	27	of	48

LAN Connector

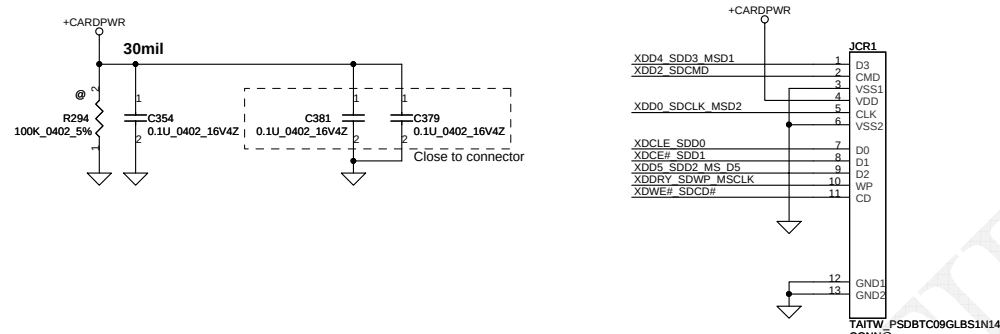


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/08/10	Deciphered Date	2009/08/10	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC,MB A6582	
Date: Friday, July 02, 2010		Sheet 28 of 48		Document Number	Rev A
				401939	

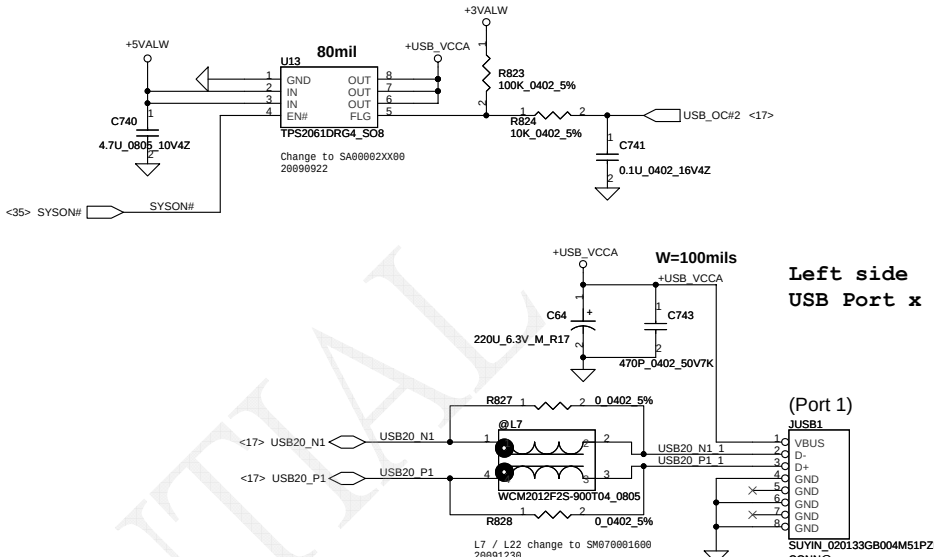
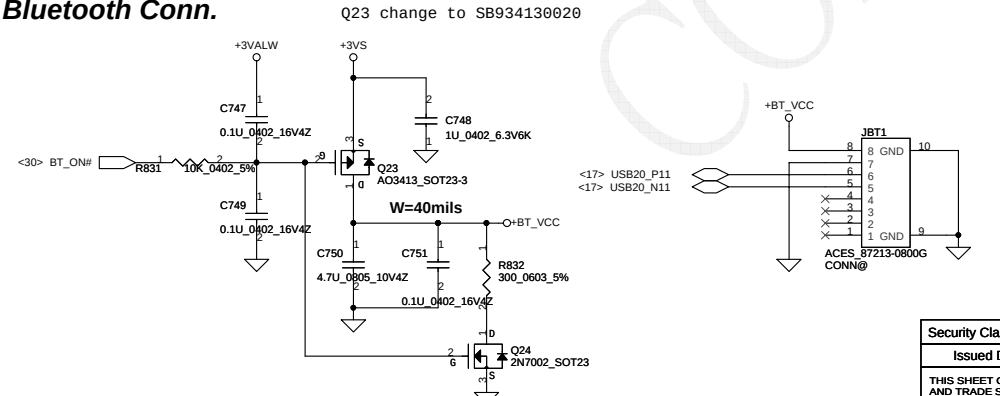
Card Reader RTS5138 / RTS5137
(only SD+MMC function)



Card Reader Connector

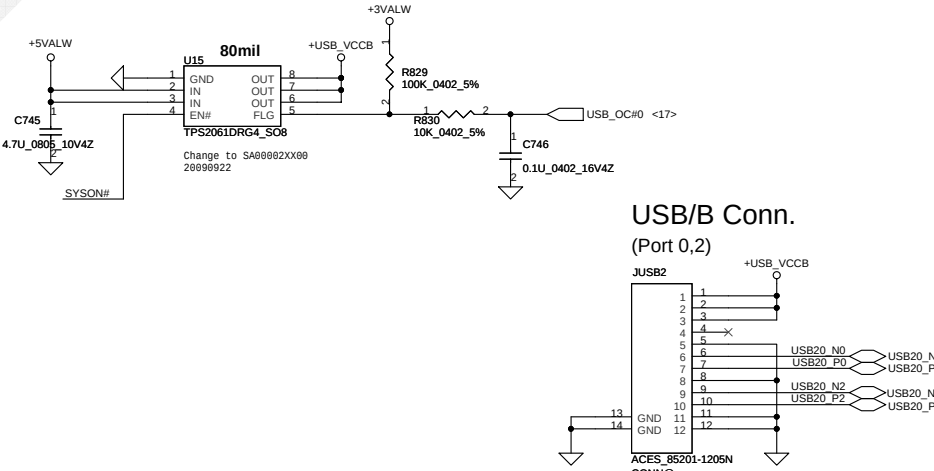


Bluetooth Conn.

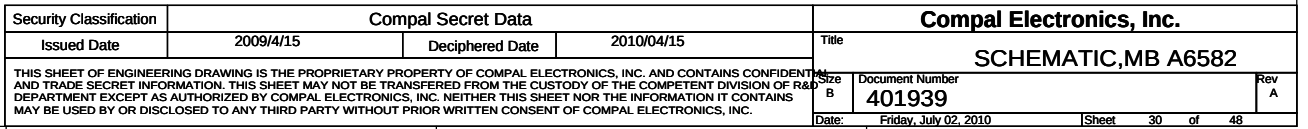


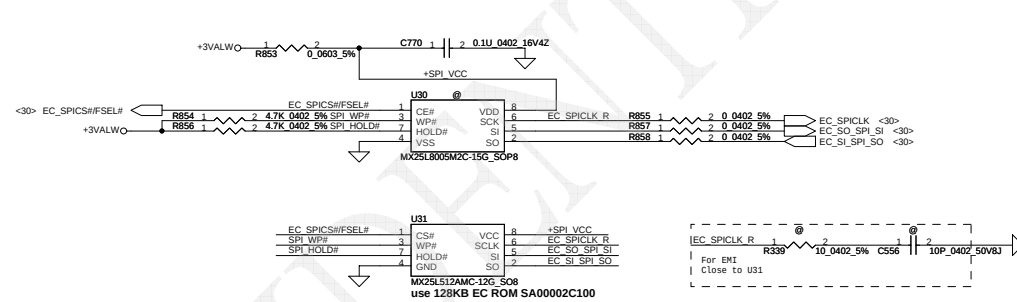
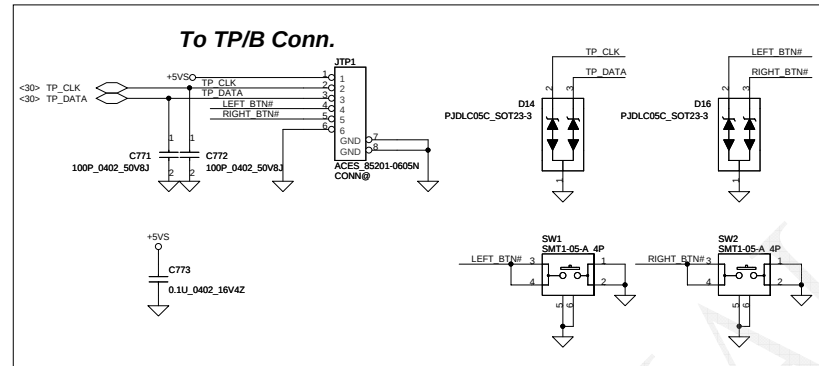
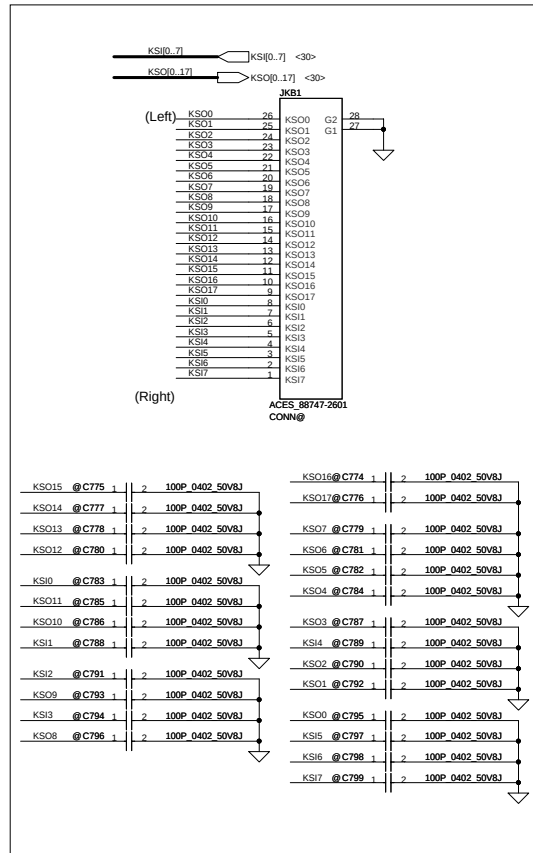
USB/B Conn.

(Port 0,2)



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2009/5/12	Deciphered Date	2009/12/31	Title	SCHEMATIC, MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAI TO ANY OTHER DIVISION OF RAI OR TO ANY OTHER COMPANY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc Size	Document Number	Rev
				B	401939	A



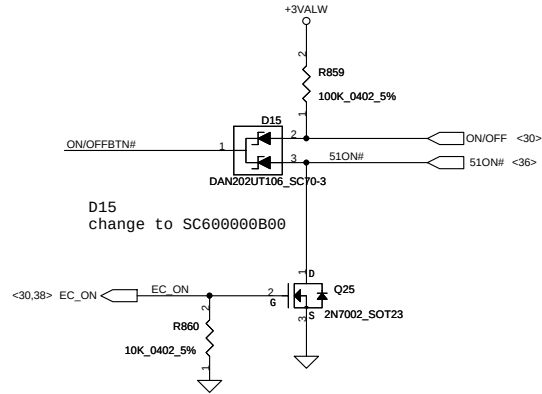


ENE suggestion SPI Frequency over 66MHz
SST: 50MHz
MXIC: 70MHz
ST: 40MHz

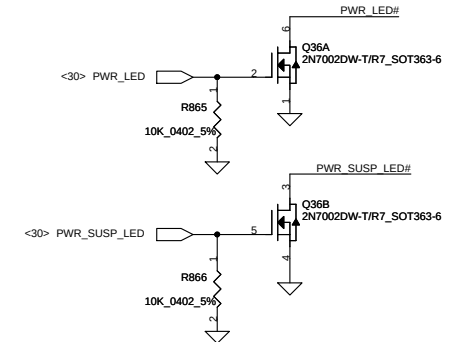
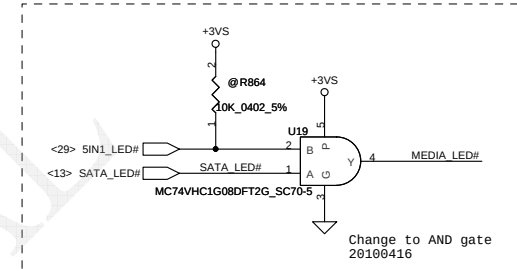
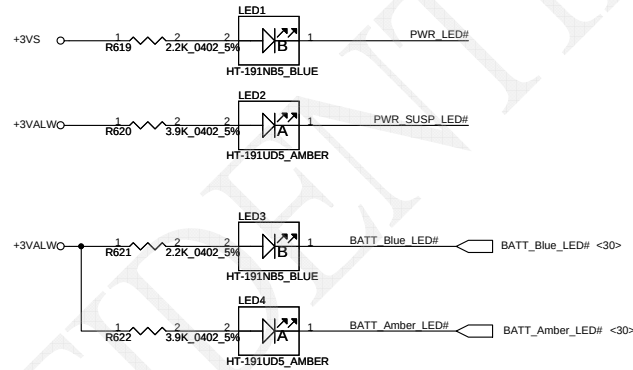
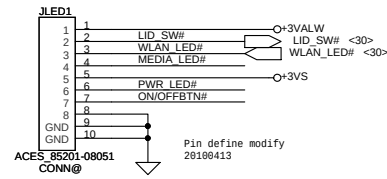
To BTN/B Conn.

KSO0	KSO3
KS11 KS12 KS13 KS14 KS15	Program_BTN# Volum up_BTN# Volum down_BTN# BT_BTN# Power save_BTN#

Power Button

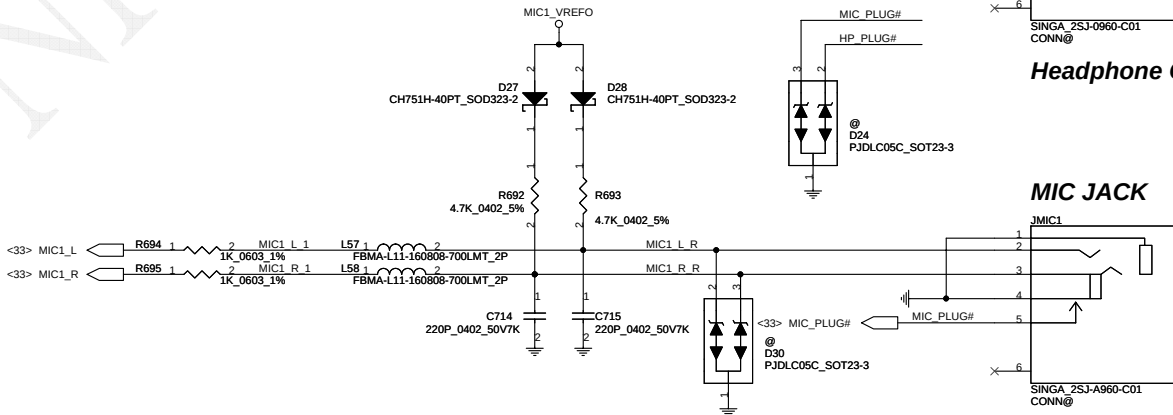
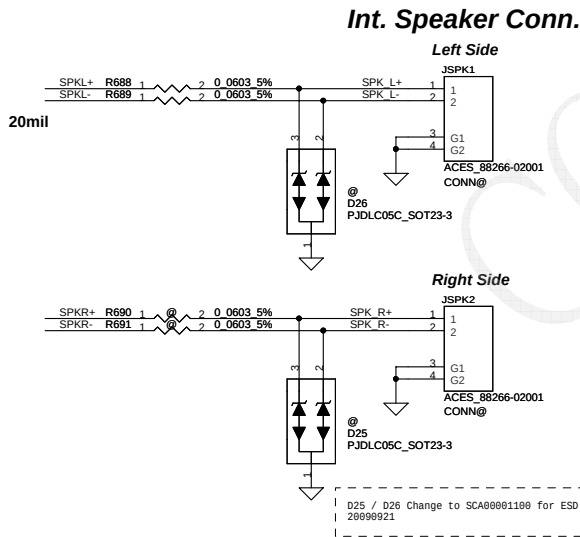
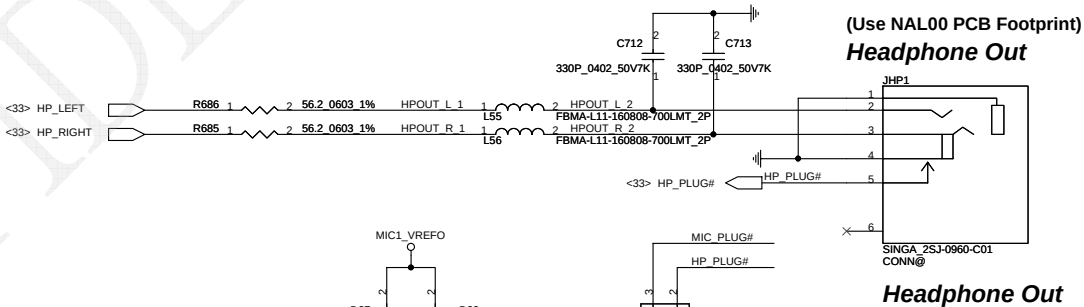
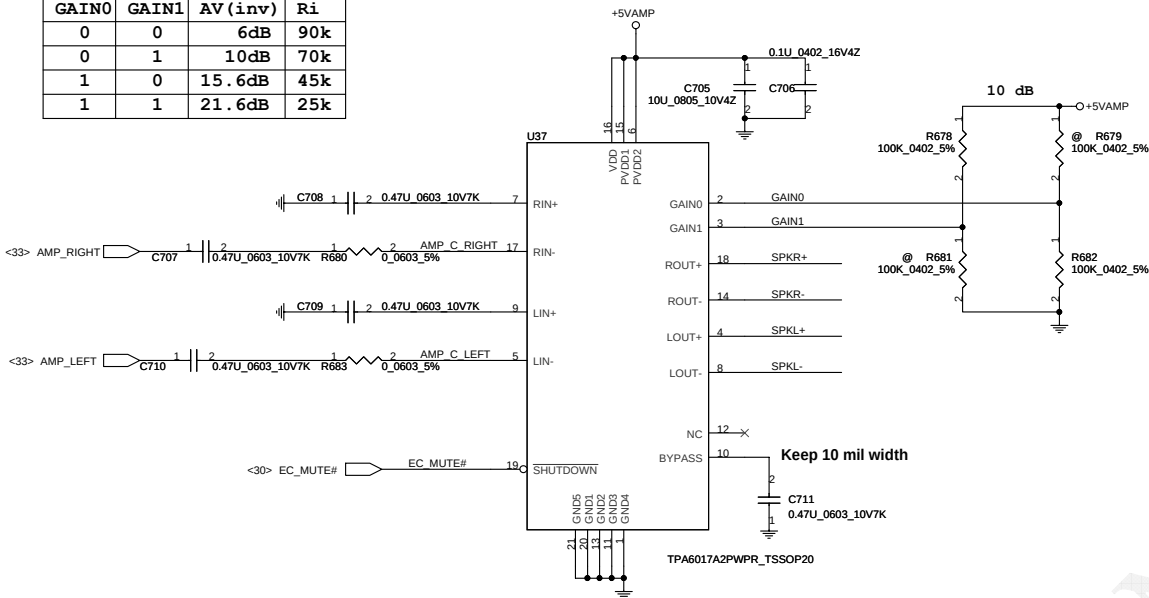


LED/B LEFT



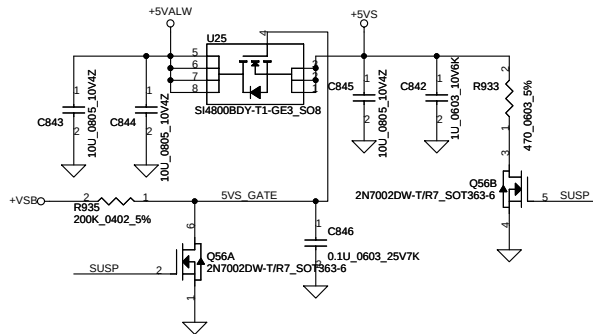
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2009/5/12				Deciphered Date			
								2009/12/31			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title				Schematic, MB A6582			
				Document Number				401939			
				Date				Friday, July 02, 2010			
				Sheet				32 of 48			

GAIN0	GAIN1	AV (inv)	Ri
0	0	6dB	90k
0	1	10dB	70k
1	0	15.6dB	45k
1	1	21.6dB	25k

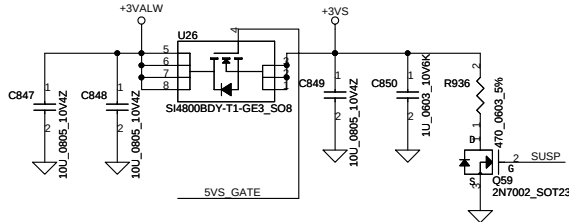


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2008/08/10				Deciphered Date			
								2009/08/10			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title				SCHEMATIC,MB A6582			
				Rev				A			
				Document Number				401939			
				Date				Friday, July 02, 2010			
				Sheet				34 of 48			

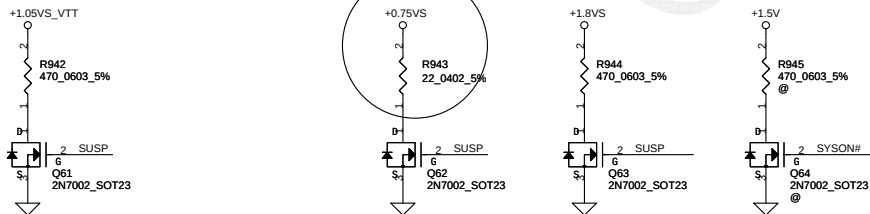
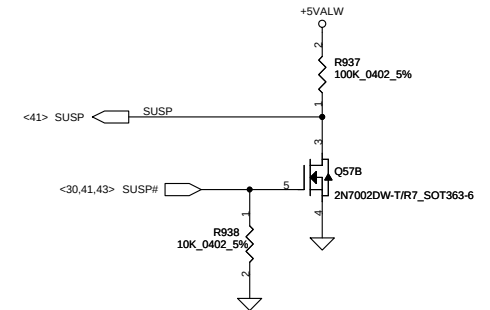
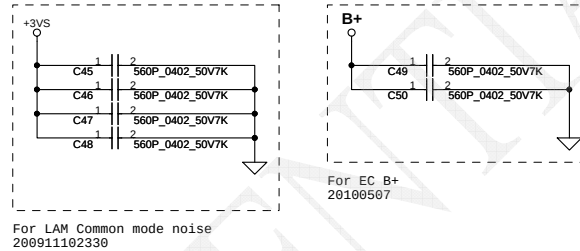
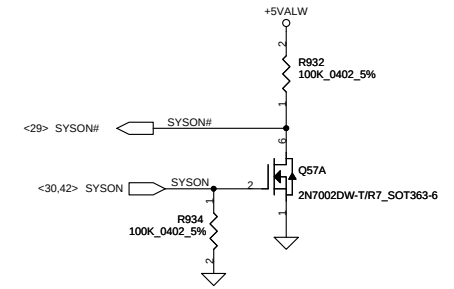
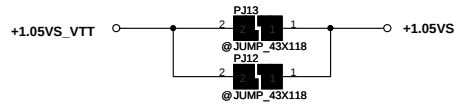
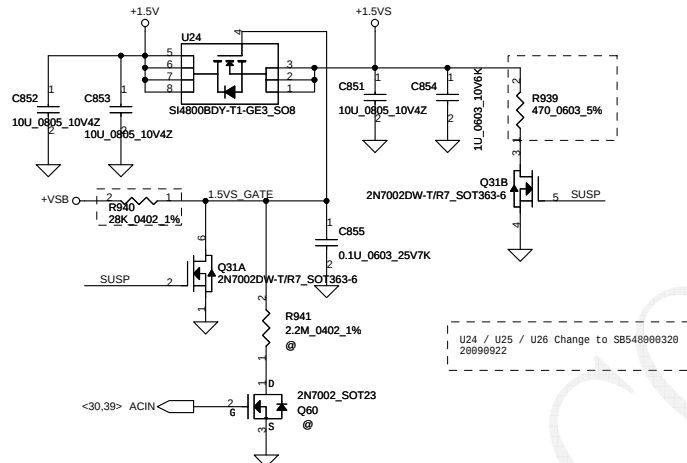
+5VALW TO +5VS



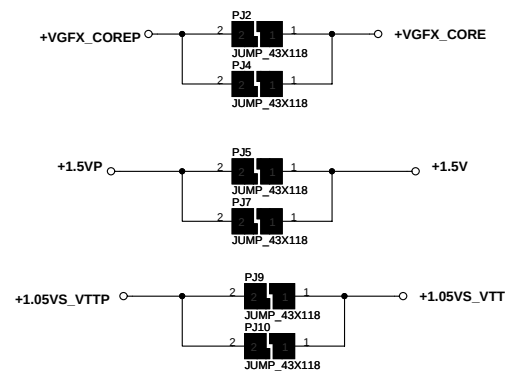
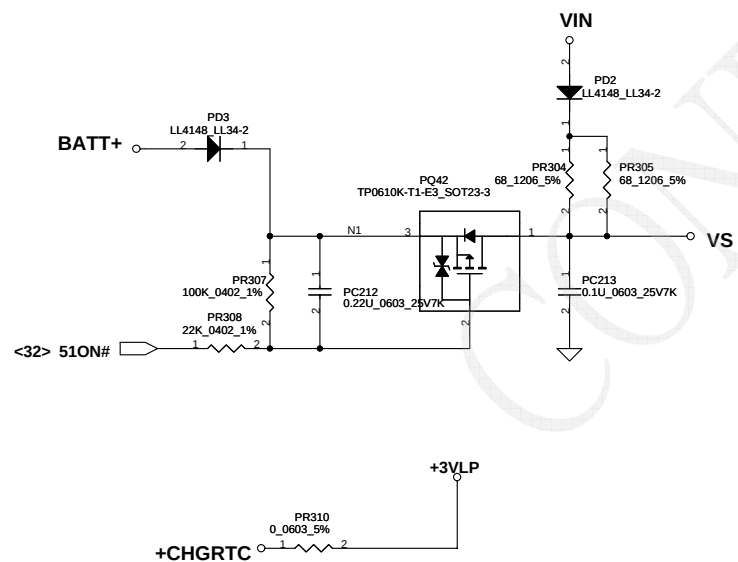
+3VALW TO +3VS



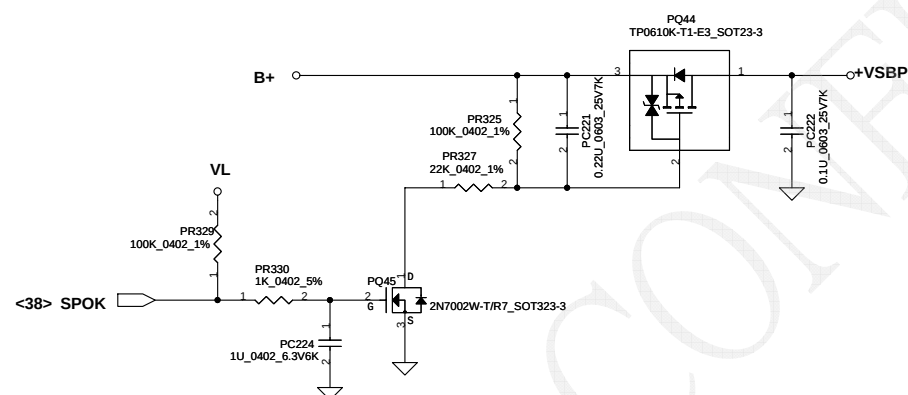
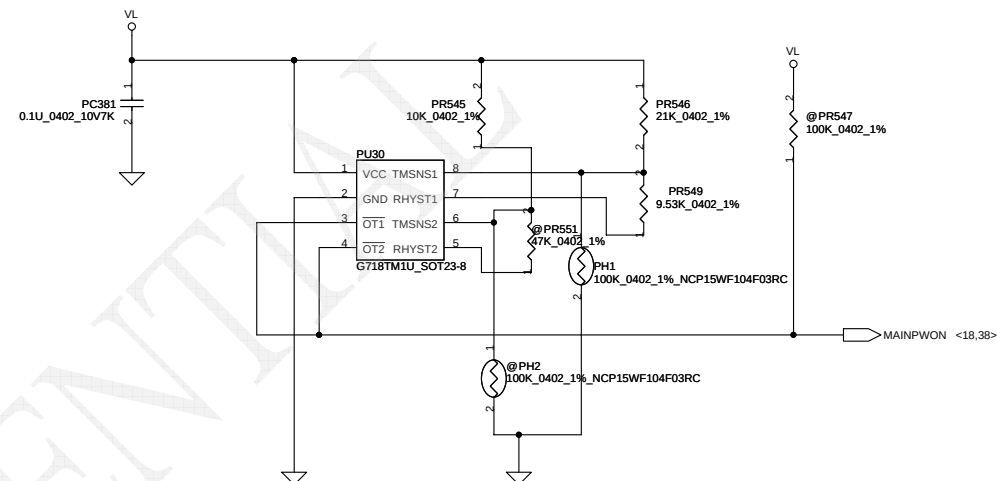
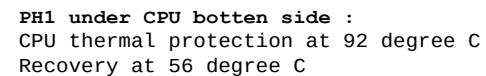
+1.5V to +1.5VS



Security Classification		Compal Secret Data				Compal Electronics, Inc.							
Issued Date		2009/5/12		Deciphered Date		2009/12/31		Title		SCHEMATIC,MB A6582			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.										Document Number		Rev A	
										401939			
										Date		Friday, July 02, 2010	

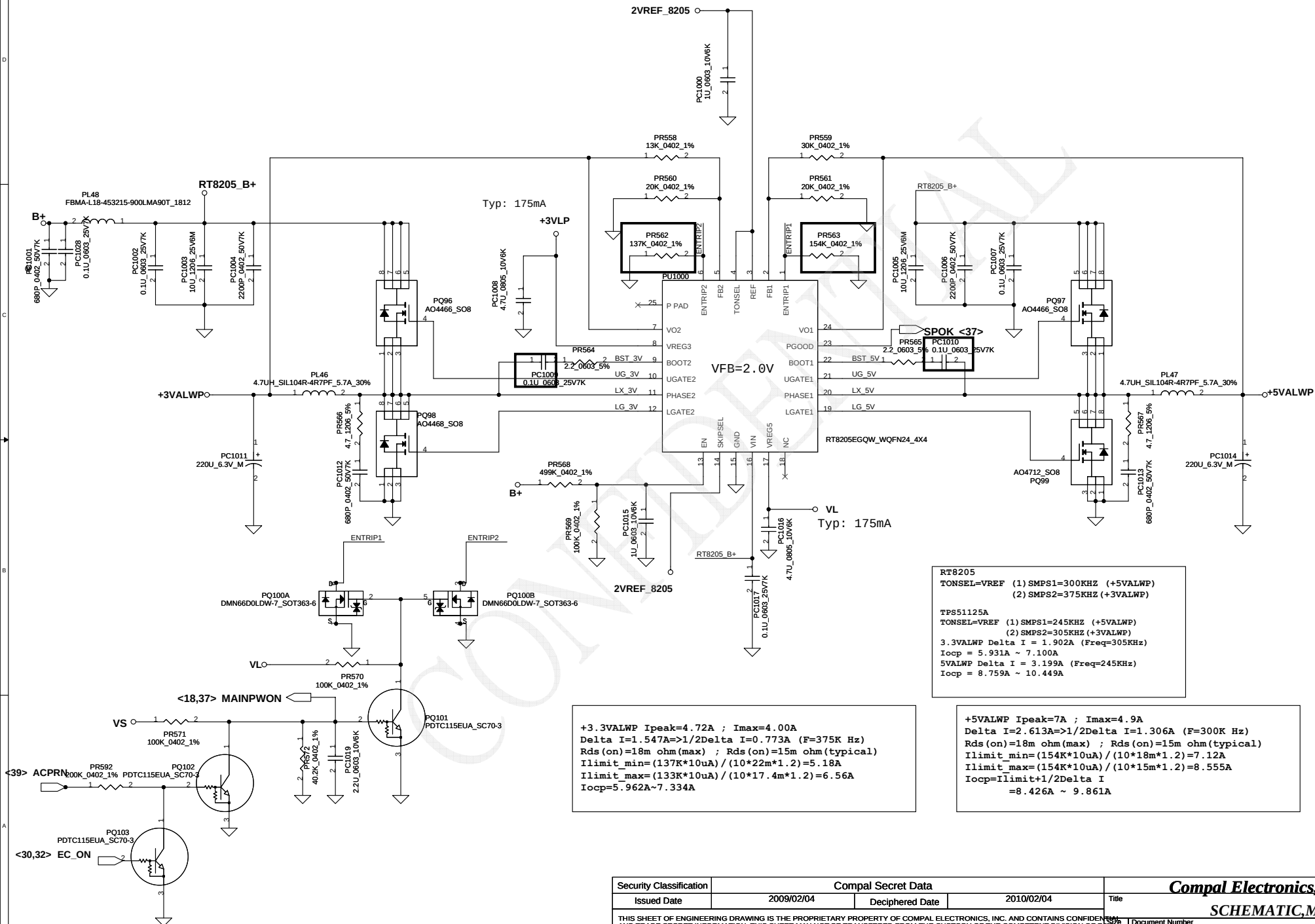


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	SCHEMATIC, MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Docuement Number	Rev
				Customer	A
Date:		Friday, July 02, 2010		Sheet	36 of 48

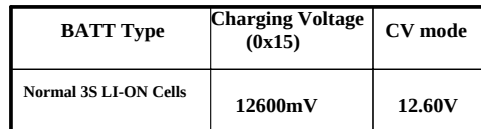


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OR DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, MB A6582		
				Rev B	Document Number	Rev A
				Custom	401939	
Date:				Friday, July 02, 2010	Sheet	37 of 48

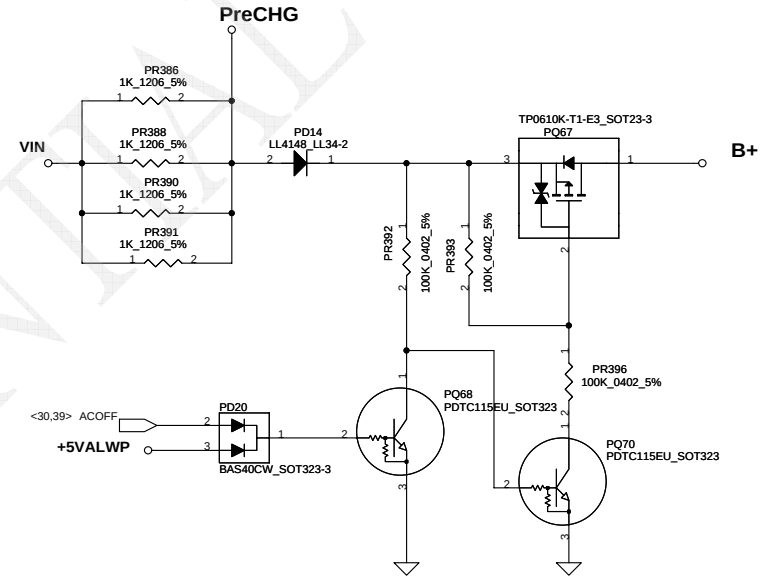
Note:
Use TPS51125 IC can remove RTC refernece LDO
Use TPS51427 IC must keep RTC refernece LDO



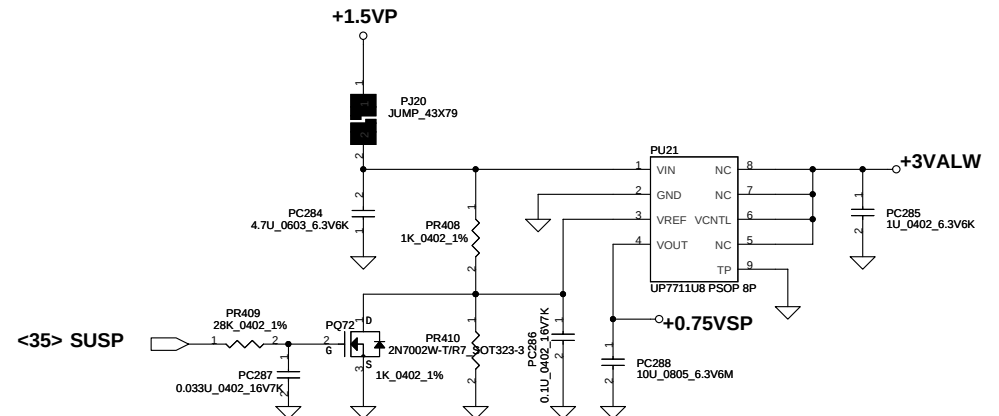
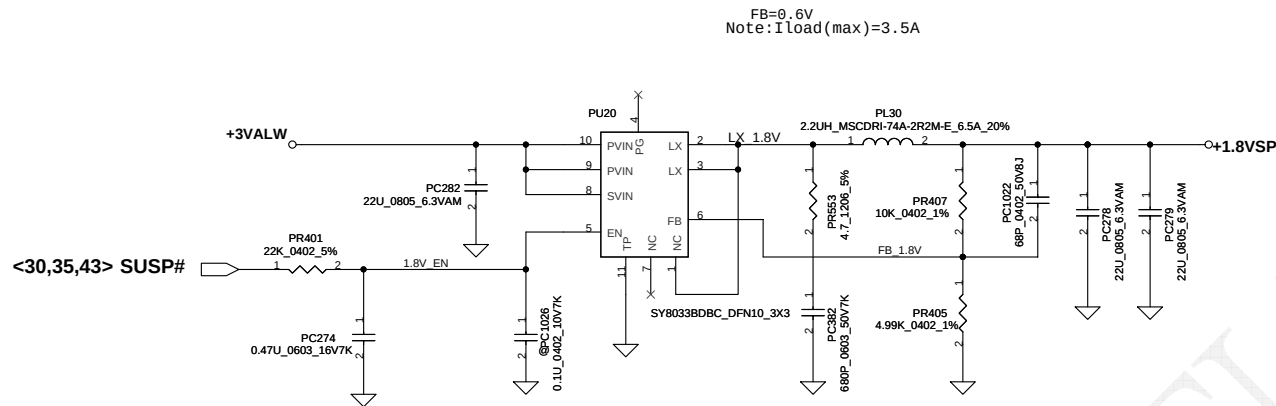
Security Classification		Compal Secret Data		Compal Electronics, Inc.					
Issued Date	2009/02/04	Deciphered Date	2010/02/04	Title	SCHEMATIC, MB A6582				
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Docu- ment Number	Rev A				
				401939					
				Date:	Friday, July 20, 2010	Sheet	38	of	48

$$\begin{aligned} \text{CP} &= 85\% \cdot I_{\text{ada}} ; \text{CP} = 4.07\text{A} \\ \text{CP} &= 85\% \cdot I_{\text{ada}} ; \text{CP} = 2.91\text{A} \end{aligned}$$


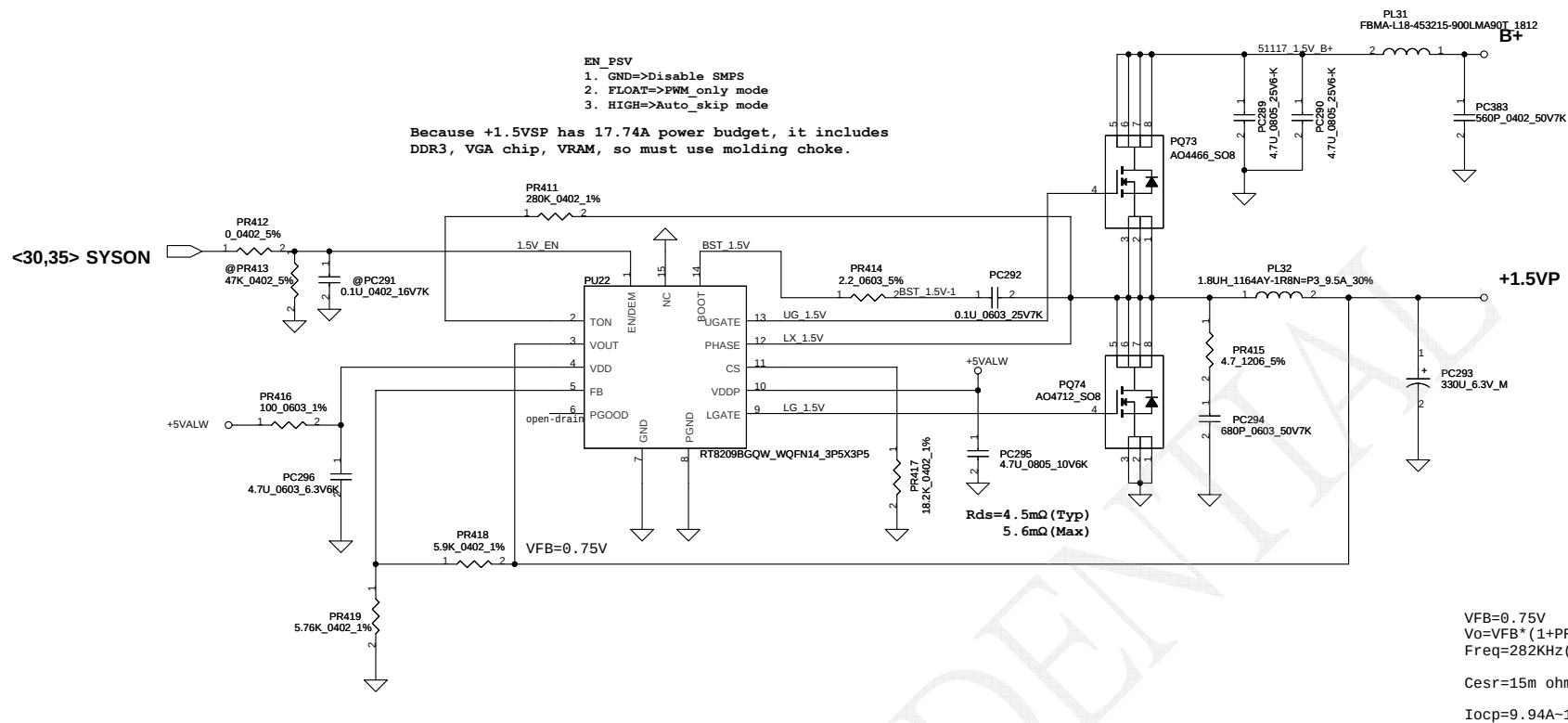
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	SCHEMATIC, MB A6582	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc Number	401939	
				Rev	A	
				Date	Friday, July 02, 2010	Sheet 39 of 48



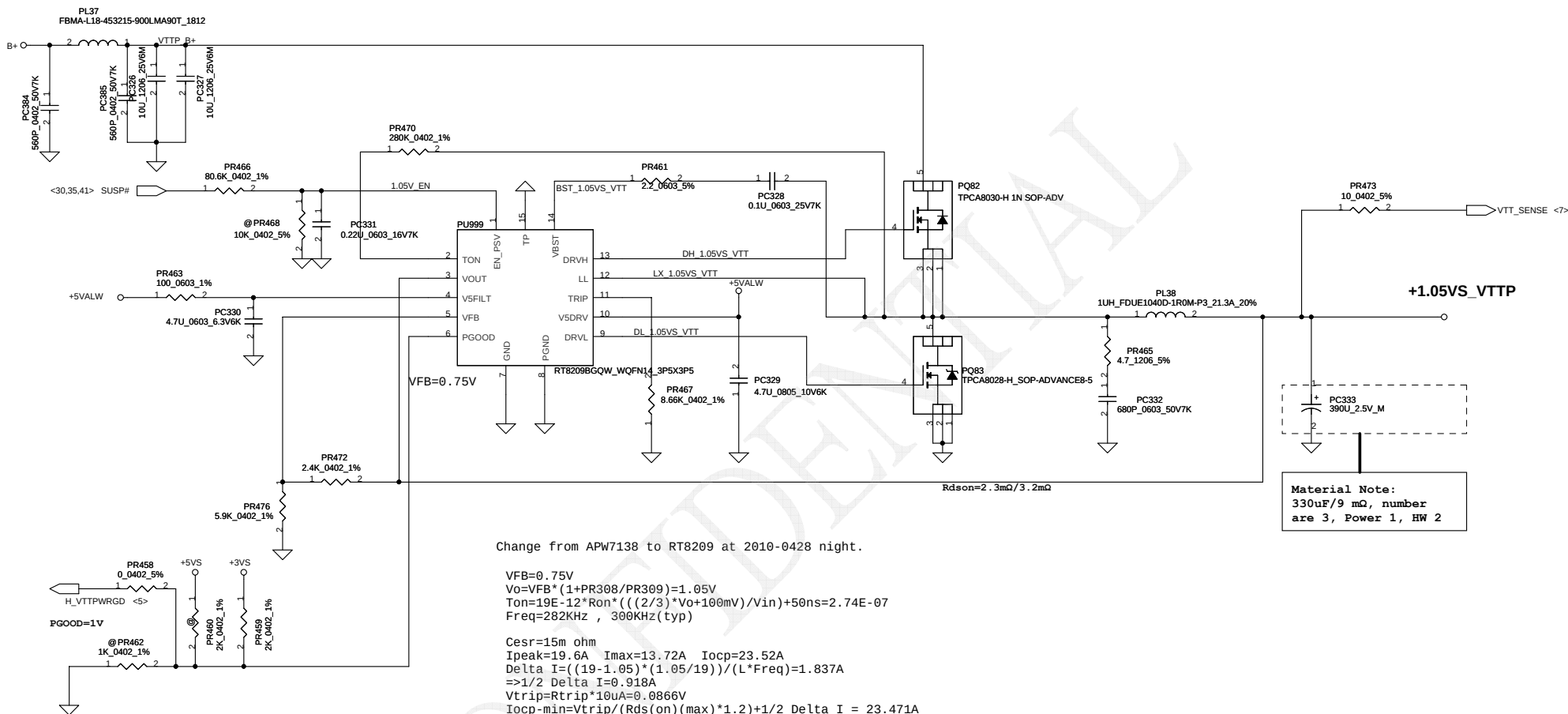
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
				Date:	Friday, July 02, 2010
				Sheet	40 of 48
				Rev	A



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				401939
				Rev A
				Date: Friday, July 02, 2010
				Sheet 41 of 48



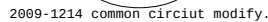
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/08/10	Deciphered Date	2009/08/10	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number 401939	Rev A
				Date: Friday, July 02, 2010	Sheet 42 of 48



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/4/15	Deciphered Date	2010/04/15	Title	SCHEMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Customer	401939
				Date	Friday, July 02, 2010
				Sheet	43 of 48
				Rev	A

$$LL = 2 * R_{droop} * G1 * DCR / R_i = 6.96m \text{ V/A}$$

where $R_{droop} = PR271 = 8.66k \text{ ohm}$, $R_i = PR283 = 1.69k \text{ ohm}$

$$I_{ocp} = OCP \text{ Threshold} * R_{droop} / LL = 24.89A$$


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	Pre-Charge circuit update.	Pre-Charge circuit disable in EVT, now modify and enable it.	0.1	39	Change PR353 from SD034470280 to SD034200380.	2010/06/08	to PVT
2					Change PR371 from SD034220280 to SD034470280.		
3					Change PR587 from SD034100380 to SD034470280.		
4	Pre-Charge circuit update.	Pre-Charge circuit disable in EVT, now modify and enable it.	0.1	38	Change PR590 from SD034200280 to SD034143280.	2010/06/08	to PVT
5	Pre-Charge circuit update.	Pre-Charge circuit disable in EVT, now modify and enable it.	0.1	40	Change PR591 from SD028470280 to SD028100380.	2010/06/08	to PVT
6	EMI fail, add 3/5V snubber and Boost Resister to solve it.	EMI request to solve EMI issue.	0.1	38	Add PC1027 SE074222K80(S CER CAP 2200P 0402 50V7K)	2010/06/08	to PVT
7	EMI fail, add 1.5V snubber.	EMI request to solve EMI issue.	0.1	42	Change PQ111 from SB000006800 to SB301150200	2010/06/08	to PVT
8					Add PQ102 and PQ103 SB301150200.		
9					Add PR592 SD034200380.		
10	EMI fail, add 1.05V snubber and Boost Resister.	EMI request to solve EMI issue.	0.1	43	Change PR386 from SD011000080 to SD011100180.	2010/06/08	to PVT
11	EMI fail, add GFX_CORE snubber.	EMI request to solve EMI issue.	0.1	44	Add PR388, PR390, PR391 SD011100180.	2010/06/08	to PVT
12	HW power sequence modify.	HW request.	0.1	43	Add PD14 SC100001Y80 LL4148_LL34-2	2010/06/08	to PVT
13					Add PR566 and PR567 SD001470B80 S RES 4.7 1206 5%		
14					Add PC1012 and PC1013 SE074681K80 S CER CAP 680P 0402 50V7K		
15	BOM unique.	BOM unique.	0.1	38	Change PR564 and PR565 from SD013000080 to SD013220B80	2010/06/08	to PVT
16	EMI request.	EMI request.	0.1	38	Add PR415 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
17	EMI request.	EMI request.	0.1	44	Add PC294 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
18	Sourcer request to change a common part.	Sourcer request.	0.1	38	Add PR465 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
19	Per sourcer request.	Sourcer request to change PC331 from SE00000R700 to SE026224K80 for common part.	0.1	43	Add PC332 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
20	CPU transient issue.	Need modify PC362 to 330P due to transient fail.	0.1	45	Change PR461 from SD013000080 to SD013220B80	2010/06/08	to PVT
21	Per sourcer request.	Per sourcer request.	0.1	43	Add PR268 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
22	Cost down.	Cost down 3VALWP and Charger Low Side MOS.	0.1	38	Add PC199 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
23	Cost down.	Cost down +1.5VP Low side MOS and choke.	0.1	42	Change PR466 from SD034576280 S RES 1/16W 57.6K +-1% 0402 to SD034806280 S RES 80.6K 0402 1%)	2010/06/08	to PVT
24	Cost down.	re-caculate 1.5VP OCP.	0.1	42	Change PC331 from SE00000R700 to SE026224K80	2010/06/08	to PVT
25			0.1	45	Change PC362 from SE074561K80 S CER CAP 560P 50V K X7R 0402 to SE074331K80 S CER CAP 330P 50V K X7R 0402	2010/06/08	to PVT
26			0.1	43	Change PQ39/PQ82 from SB000008L80 to SB00000HL00.	2010/06/08	to PVT
27			0.1	38	Change PQ98 from SB00000AJ00 to SB000009580(A04468).	2010/06/08	to PVT
28			0.1	39	Change PQ64 from SB00000CG00 to SB000009580(A04468).	2010/06/08	to PVT
29			0.1	42	Change PQ74 from SB000009F80 to SB00000AJ00(A04712).	2010/06/08	to PVT
30			0.1	42	Change PL32 from SH000009U00 to SH000009680.	2010/06/08	to PVT
31			0.1	42	Change PR417 from SD034110280 to SD034182280.	2010/06/08	to PVT

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	SCHMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	401939
Date:	Friday, July 02, 2010	Sheet	46	of	48

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
24	Cost down.	re-caculate +3VALWP OCP.	0.1	38	Change PR562 from SD034107380 to SD034137380.	2010/06/08	to PVT
25							
26							
27							
28							
29							
30							
31							
32							
33							
34							
35							
36							
37							
38							
39							
40							
41							
42							
43							
44							
45							
46							

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/09/20	Deciphered Date	2008/09/20	Title	SCHMATIC,MB A6582
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev A	
Date: Friday, July 02, 2010		Sheet 47 of 48			

Version change list (P. I. R. List)

Item	Phase	PAGE	DATE	Modifycatio list	Purpose
1	EVT		04 / 08 04 / 16 04 / 21 04 / 26 04 / 27 04 / 29 04 / 29 05 / 03 05 / 03 05 / 06 05 / 07 05 / 10 05 / 11 05 / 12 05 / 13	N/A Modify RTC SCH Change LAN to AR8152 Update Power SCH Update AND Gate symbol - U1 / U2 / U6 / U7 / U19 Update Power MOS symbol - U24 / U25 / U26 C196 change to 1U for INTEL disign CLK GEN del C35 / C36 / C30 / C31 / C32 / C33 / C34 / L3 (for 3G@ and @ function) Q4+Q6 change to 2N7002DW Update U8 symbol Add DMIC function Modify USB define. Update Power SCH Add R17 for DMIC power Update Power SCH Change LAN to GIGA&10/100 co-lay Del R307 (LVDS conn.) Change +1.05VS_VTT to +1.05VS (CLK GEN) Change C842 / C850 / C854 / C656 to SE080105K80 1U_0603_10V6K Change R940 to 28K (S3 Power sequence) SW T1 pin define (LAN) Change R907 (0.0603) to 0.1206 5% Remove C40 / C41 / C42 / C43 / C44 Change FAN Conn. Update Power SCH Change LAN to BCM57780 GIGA Del R637 / R639 / R640 / R645 / R647 / R648 (AUDIO) Del DMIC Update Power SCH Update Power SCH Add C49 / C50 (EC B+) Update Power SCH C764 / C765 change to 18P_0402_50V8J (EC) Reserve D13 / D24 / D30 (ESD) U17 change to RTS5137 (SA000043500) Card reader Update Power SCH	
			06 / 01 06 / 02 06 / 03 06 / 04 06 / 10	Unpop L11 / C247 & Pop R304 for cost down Unpop C764 / X2 & Pop R13 & C765 Change to 100K for EC remove Crystal Pop R834 & R835 change to 200K for Project ID Unpop D24 / D30 Unpop R690 / R691 Pop C49 / C50 For HDMI Unpop R753 / R757 , Add R754 / R758 to 0 ohm. Change R759 to 3.9K. Change R755 to 4.7K ohm , Unpop R748. Unpop R778. R833 / R292 0.0805 change to 0.0603. C190 / C191 & C1135 / C1136 change to 33P 0402 50V8J for Vender test report Unpop C774-C799 for EMI cost down. Update Power SCH Add T25 / T26 / T27	

www.s-manuals.com